



**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
 YELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2021- 22

SEMESTER : III  
 BRANCH : ECE

Wef: 21/10/2021

SECTION : A  
 CLASS ROOM : A- 124(CR 105)  
 CLASS TEACHER: Prof. Chandrabhabha R

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 -9.30	9.30 -10.30	10.30 -10.50	10.50 - 11.50	11.50 - 12 50	12 50- 1 45	1 45 - 2. 40	2.40 - 3.35	3.35 - 4.30
MON	PE&I	M3		CO	NT				
TUE	NT	ED		DSD	M3		PLACEMENT	DIPLOMA MATHS	
WED	EDI LAB A1 (SKJ, CP) DSD LAB A2(VB, TT)				CO		NT	PE&I	
THUR	DSD	NT		CIP	M3		DSD LAB A1 (VB, AKD,SH) EDI LAB A2 (SKJ, CP)		
FRI	M3	CO		ED	DSD		ADDITIONAL MATHS	PROCTORING	
SAT	ED	PE&I							

SUBJECT	SUBJECT CODE	CREDITS	COURSE COORDINATOR
MATHS 3	18MAT31	3	DR. JOJO JOSEPH IDICULA
NETWORK THEORY	18EC 32	4	DR. RASHMI N
ELECTRONIC DEVICES	18EC 33	3	DR. JAYADEVA G S
DIGITAL SYSTEM DESIGN	18EC 34	3	DR. C S MALA
COMPUTER ORGANIZATION & ARCHITECTURE	18EC 35	3	PROF. SURYAKANTH B M
POWER ELECTRONICS & INSTRUMENTATION	18EC 36	3	PROF. CHANDRAPRABHA R
ELECTRONIC DEVICES & INSTRUMENTATION LABORATORY	18ECL37	2	PROF. SHASHIKALA J
DIGITAL SYSTEM DESIGN LABORATORY	18ECL38	2	PROF. VINUTHA B
CONSTITUTION OF INDIA, PROFESSIONAL ETHICS AND CYBER LAW	18ECL39	1	PROF. TEJASWINI

Time Table Officers  
 DR RVB

Chief TTD

HOD

Dea. Academics

Principal



**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
 YELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2021- 22

SEMESTER : III  
 BRANCH : ECE

Wef : 21/10/2021

SECTION : B  
 CLASS ROOM : A 223(CR 206)  
 CLASS TEACHER: Dr. Prachi Sharma

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30	
MON	NT	M3		DSD	PE&I		EDI LAB B1 (CP, SRG) DSD LAB B2(AK, T T)			
TUE	ED	PE&I		NT	M3		CIP	DIPOLMA MATHS		
WED	CO	ED		M3	NT					
THUR	PE&I	ED		DSD	CO					
FRI	DSD LAB B1(AK, SH) EDI LAB B2 (CP, SK)				M3		ADDITIONAL MATHS	PROCTORING		
SAT	DSD	CO		NT	PLACEMENT					

Subject	Subject Code	Credits	Course coordinator
MATHS 3	18MAT31	3	PROF. NEHA
NETWORK THEORY	18EC32	4	DR. DANKAN GOWDA V
ELECTRONIC DEVICES	18EC33	3	DR. PRACHI SHARMA
DIGITAL SYSTEM DESIGN	18EC34	3	DR. AMIT KUMAR
COMPUTER ORGANIZATION & ARCHITECTURE	18EC35	3	DR. HANUMANTHARAJU
POWER ELECTRONICS & INSTRUMENTATION	18EC36	3	PROF. CHANDRAPRABHA R
ELECTRONIC DEVICES & INSTRUMENTATION LABORATORY	18ECL37	2	PROF. CHANDRAPRABHA R
DIGITAL SYSTEM DESIGN LABORATORY	18ECL38	2	DR. AMIT KUMAR
CONSTITUTION OF INDIA, PROFESSIONAL ETHICS AND CYBER LAW	18CPC39	1	PROF. TEJASWINI

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2021-22

SEMESTER : III  
 BRANCH : ECE

Wef: 21/10/2021

SECTION : C  
 CLASS ROOM : A 224 (CR 205)  
 CLASS TEACHER: Dr. Anitha V R

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	EDI LAB C1 (AVR, SRG) DSD LAB C2 (SH, VB, TT)				ED		M3	CIP	
TUE	DSD LAB C1 (SH, AK) EDI LAB C2 (AVR, SKI)				PE&I		NT	DIPLOMA MATHS	
WED	PE&I	NT		M3	ED				
THUR	NT	CO		DSD	M3				
FRI	CO	NT		PE&I	DSD		ADDITIONAL MATHS	PROCTORING	
SAT	M3	ED		DSD	CO				

Subject	Subject Code	Credits	Course coordinator
MATHS 3	18MAT31	3	DR. PUSHPA B V
NETWORK THEORY	18EC32	4	DR. SUREKHA R GONDKAR
ELECTRONIC DEVICES	18EC33	3	DR. ANITHA V R
DIGITAL SYSTEM DESIGN	18EC34	3	PROF. SHILPA HIREMATH
COMPUTER ORGANIZATION & ARCHITECTURE	18EC35	3	DR. HANUMANTHARAJU
POWER ELECTRONICS & INSTRUMENTATION	18EC36	3	PROF SABINA RAHMAN
ELECTRONIC DEVICES & INSTRUMENTATION LABORATORY	18ECL37	2	DR. ANITHA V R
DIGITAL SYSTEM DESIGN LABORATORY	18ECL38	2	PROF. SHILPA HIREMATH
CONSTITUTION OF INDIA, PROFESSIONAL ETHICS & CYBER LAW	18CPC39	1	PROF. TEJASWINI

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 TIME TABLE FOR THE ACADEMIC YEAR 2021-22

SEMESTER : V  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM : A 220 (CR 207)  
 CLASS TEACHER: PROF. SHIVARUDRAIAH B

Wef : 21/10/2021

	8.30 -9.30	9.30 -10.30	10.30 -10.50	10.50 - 11.50	11.50 - 12.50	12.50-1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	VHDL	PCS		DSP	EVS		DSP LAB A1(SCT, DG,VB) HDL LAB A2(SB, SBM)		
TUE	PCS	VHDL		TIME	DSP				
WED	HDL LAB A1(SB, SBM) DSP LAB A2(SCT, DG)				ITC		PLACEMENT		
THUR	DSP	TIME		EMW	ITC				
FRI	TIME	PCS		EMW	DSP				
SAT	EMW	ITC		PCS	VHDL				

SUBJECT	SUBJECT CODE	CREDITS	COURSE COORDINATOR
TECHNOLOGICAL INNOVATION MANAGEMENT AND ENTREPRENEURSHIP	18ES51	3	PROF. THYAGARAJ T
DIGITAL SIGNAL PROCESSING	18EC52	4	DR G.V. VIJAYALAKSHMI
PRINCIPLES OF COMMUNICATION SYSTEMS	18EC53	4	DR. AMBIKA R
INFORMATION THEORY & CODING	18EC54	3	PROF. VINUTHA B
ELECTROMAGNETIC WAVES	18EC55	3	PROF. SANEESH CLEATUS T
VERILOG HDL	18EC56	3	PROF. SHIVARUDRAIAH B
DIGITAL SIGNAL PROCESSING LABORATORY	18EC57	2	PROF. SANEESH CLEATUS T
HDL LABORATORY	18ECL57	2	PROF. SHIVARUDRAIAH B
ENVIRONMENTAL STUDIES	18ECL58	1	DR. CHANDRASHEKHARAPPA

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2021-22

SEMESTER : V  
 BRANCH : ECE

Wef : 21/10/2021

SECTION : B  
 CLASS ROOM : A 302 (CR 302)  
 CLASS TEACHER : DR. DANKAN GOWDA V

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	ITC	DSP		TIME	PCS				
TUE	VHDL	PCS		ITC	EVS				
WED	DSP	EMW		TIME	PLACEMENT				
THUR	TIME	DSP		VHDL	PCS				
FRI	EMW	PCS		VHDL	DSP				
SAT	HDL LAB B1 (SBM, LS) DSP LAB B2 (ASR, TT)				EMW				

Subject	Subject code	Credits	Course coordinator
TECHNOLOGICAL INNOVATION MANAGEMENT AND ENTREPRENEURSHIP	18ES51	3	DR. ANIL KUMAR D
DIGITAL SIGNAL PROCESSING	18EC52	4	DR. A. SHOBHA RANI
PRINCIPLES OF COMMUNICATION SYSTEMS	18EC53	4	DR. DANKAN GOWDA V
INFORMATION THEORY & CODING	18EC54	3	PROF. SHASHIKALA J
ELECTROMAGNETIC WAVES	18EC55	3	DR. ANITHA V R
VERILOG HDL	18EC56	3	PROF. SURYAKANTH B
DIGITAL SIGNAL PROCESSING LABORATORY	18EC57	2	DR. A. SHOBHA RANI
HDL LABORATORY	18ECL57	2	PROF. SURYAKANTH B
ENVIRONMENTAL STUDIES	18ECL58	1	DR. CHANDRASHEKHARAPPA

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2021- 22

Wef : 2/10/2021

SEMESTER : V  
 BRANCH : ECE

SECTION : C  
 CLASS ROOM : A 303(CR 303)  
 CLASS TEACHER : Dr. Amit Kumar

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	EMW	DSP		ITC	TIME		PLACEMENT		
TUE	DSP LAB C1(DR, GVV) HDL LAB C2(LS, SB...)				PCS				
WED	PCS	DSP		TIME	EVS		DSP LAB C2(DR, GVV) HDL LAB C1(LS, AKD)		
THUR	VHDL	EMW		PCS	DSP				
FRI	DSP	TIME		VHDL	ITC				
SAT	PCS	ITC		EMW	VHDL				

Subject	Subject code	Credits	Course coordinator
TECHNOLOGICAL INNOVATION MANAGEMENT AND ENTREPRENEURSHIP	18ES51	3	DR. A. SHOBHA RANI
DIGITAL SIGNAL PROCESSING	18EC52	4	DR. DEEPA N. REDDY
PRINCIPLES OF COMMUNICATION SYSTEMS	18EC53	4	DR. AMBIKA R
INFORMATION THEORY & CODING	18EC54	3	PROF. SHASHIKALA J
ELECTROMAGNETIC WAVES	18EC55	3	DR. AMIT KUMAR
VERILOG HDL	18EC56	3	PROF. LAXMISAGAR
DIGITAL SIGNAL PROCESSING LABORATORY	18EC57	2	DR. DEEPA N. REDDY
HDL LABORATORY	18ECL57	2	PROF. LAXMISAGAR
ENVIRONMENTAL STUDIES	18ECL58	1	DR. CHANDRASHEKHARAPPA

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

*Dr. G*  
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 DR & VR

*Z*  
 Chief TTD

*H*  
 HOD

*Z*  
 Dean Academics

*Z*  
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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
 YELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2021-22

Wef : 21/10/2021

SEMESTER : VII  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM :  
 CLASS TEACHER: Prof. Shilpa Hiremath

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	OE	CCN LAB A1 (RVA, PS)					IOT/CRYPTO/ML	SATCOM/RTS	CCN
TUE	IOT/CRYPTO/ML	VLSI		OE	SATCOM/RTS		CCN LAB A2 (RVA, MKR)		
WED	CCN	IOT/CRYPTO/ML		OE	SATCOM/RTS		VLSI LAB A1(SR, RN)		
THUR	VLSI LAB A2 (SR, DG)				VLSI				
FRI	VLSI	CCN		SEMINAR	PLACEMENT		PROCTORING		
SAT	PROJECT WORK								

Subject	Subject code	Credits	Course coordinator	
COMPUTER NETWORKS	18EC71	3	PROF. RAVINDRA ASUNDI	
VLSI DESIGN	18EC72	3	PROF SABINA RAHMAN	
PROFESSIONAL ELECTIVE-2	REAL TIME SYSTEM	18EC731	3	PROF. SHIVARUDRAIAH B
	SATELLITE COMMUNICATION	18EC732	3	PROF. SHILPA HIREMATH
PROFESSIONAL ELECTIVE-3	IOT & WIRELESS SENSOR NETWORKS	18EC741	3	PROF. VINUTHA B
	CRYPTOGRAPHY	18EC744	3	PROF MAMATHA K R
	MACHINE LEARNING	18EC745	3	DR. ANIL KUMAR D
OPEN ELECTIVE	18XX75X	3		
COMPUTER NETWORKS LABORATORY	18ECL76	2	PROF. RAVINDRA ASUNDI	
VLSI LABORATORY	18ECL77	2	PROF SABINA RAHMAN	

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2021- 22

Wef : 21/10/2021

SEMESTER : VII  
 BRANCH : ECE

SECTION : B  
 CLASS ROOM :  
 CLASS TEACHER: Prof. Jagannatha K B

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30	
MON	OE	CCN		VLSI	CCN		IOT/CRYPTO/ML	SATCOM/RTS		
TUE	IOT/CRYPTO/ML	VLSI		OE	SATCOM/RTS		SEMINAR			
WED	VLSI	IOT/CRYPTO/ML		OE	SATCOM/RTS					
THUR	PROJECT WORK						VLSI LAB B1 (JKB, SR)			
FRI	CCN LAB B2 (MKR, RVA) ETE LAB				CCN		CCN LAB B1 (MKR, PS) ETE LAB			
SAT	VLSI LAB B2 (JKB, SR)				PLACEMENT					

Subject	Subject code	Credits	Course coordinator
COMPUTER NETWORKS	18EC71	3	PROF MAMATHA K R
VLSI DESIGN	18EC72	3	PROF. JAGANNATHA K B
PROFESSIONAL ELECTIVE-2	REAL TIME SYSTEM	3	PROF. SHIVARUDRAIAH B
	SATELLITE COMMUNICATION	3	DR. DEEPA N. REDDY
	IOT & WIRELESS SENSOR NETWORKS	3	PROF. VINUTHA B
PROFESSIONAL ELECTIVE-3	CRYPTOGRAPHY	3	PROF MAMATHA K R
	MACHINE LEARNING	3	DR. ANIL KUMAR D
OPEN ELECTIVE	18XX75X	3	
COMPUTER NETWORKS LABORATORY	18ECL76	2	PROF MAMATHA K R
VLSI LABORATORY	18ECL77	2	PROF. JAGANNATHA K B

Time Table Officers  
 DR & VR

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 YELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2021-22

SEMESTER : VII  
 BRANCH : ECE

SECTION : C  
 CLASS ROOM : BSN TR 502  
 CLASS TEACHER: Prof. Laxmi Sagar H S

Wef : 21/10/2021

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	OE	VLSI		SEMINAR	PLACEMENT		IOT/CRYPTO/ML	SATCOM/RTS	
TUE	IOT/CRYPTO/ML	CCN		OE	SATCOM/RTS				
WED		IOT/CRYPTO/ML		OE	SATCOM/RTS		VLSI	CCN	
THUR	VLSI	CCN							
FRI	VLSI LAB C1 (RN, SB) (8.30-11.15) JKB			CCN LAB C2 (PS, AK) (11.15-1.30)		VLSI LAB C2 (RN, JKB)			
SAT	CCN LAB C1 (PS, RVA) ETE LAB								

SUBJECT	SUBJECT CODE	CREDITS	COURSE COORDINATOR	
COMPUTER NETWORKS	18EC71	3	PROF. RAVINDRA ASUNDI	
VLSI DESIGN	18EC72	3	PROF. JAGANNATHA K B	
PROFESSIONAL ELECTIVE-2	REAL TIME SYSTEM	18EC731	3	PROF. SHIVARUDRAIAH B
	SATELLITE COMMUNICATION	18EC732	3	PROF. LAXMISAGAR H S
	IOT & WIRELESS SENSOR NETWORKS	18EC741	3	PROF. VINUTHA B
PROFESSIONAL ELECTIVE-3	CRYPTOGRAPHY	18EC744	3	PROF MAMATHA K R
	MACHINE LEARNING	18EC745	3	DR. ANIL KUMAR D
OPEN ELECTIVE	18XX75X	3		
COMPUTER NETWORKS LABORATORY	18ECL76	2	DR. PRACHI SHARMA	
VLSI LABORATORY	18ECL77	2	PROF RASHMI N	

Time Table Officers  
 DR & VR

Chief TTD

HOD

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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**YELAHANKA - BANGALORE - 64**  
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 TIME TABLE FOR THE ACADEMIC YEAR 2021-22

Wef : 21/10/2021

SEMESTER : VII  
 BRANCH : ECE

**PARALLEL PROMOTED STUDENTS TIME TABLE**

SECTION : A,B,C  
 CLASS ROOM : A, B,C classrooms allocated for Electives  
 DIP,M&A, PE- R&D Lab

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	POWER ELECTRONICS			SEMINAR			CRYPTO	SATCOM	M&A
TUE	CRYPTO	DIP		M&A	SATCOM		M&A	POWER ELECTRONICS	DIP
WED	M&A	CRYPTO		DIP	SATCOM		DIP	POWER ELECTRONICS	
THUR	VLSI LAB A2(SR,DG)				POWER ELECTRONICS		ADC LAB(SRG,RN)		
FRI	PROJECT WORK						VLSI LAB C2(RN,JKB)		
SAT	VLSI LAB B2(JKB,SR)								

Subject	Subject code	Credits	Course coordinator
MICROWAVES AND ANTENNAS	15ECT1 / 17EC71	4	DR. RASHMI N
VLSI DESIGN	15ECT2 / 17EC72	4	DR. SUREKHA G
PROFESSIONAL ELECTIVE-4 SATELLITE COMMUNICATION	15ECT55 / 17EC745	3	PROF. SHILPA HIREMATH / PROF DEEPA REDDY PROF. LAKSHMISAGAR
PROFESSIONAL ELECTIVE-3 CRYPTOGRAPHY	15ECT44 / 17EC744	3	PROF MAMATHA K R
POWER ELECTRONICS	15ECT3 / 17EC73	4	PROF. THYAGARAJ T
ADVANCED COMMUNICATION LAB	15ECT76 / 17ECL76	2	DR. SUREKHA G
VLSI LABORATORY	15ECT1 / 17ECL77	2	PROF SABINA RAHMAN / PROF JKB / PROF RASHMI

Time Table Officers DR & VH      Chief TTO      HOD      Dean Academics      Principal



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YELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
TIME TABLE FOR THE ACADEMIC YEAR 2020-21

Wef : 19/04/2021

SEMESTER : IV  
BRANCH : ECE

SECTION : A  
CLASS ROOM : CR 105  
CLASS TEACHER: PROF. SHIVRUDRAIAH B

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	AC	MC		CS	ESLA		SS		
TUE	MC	MATHS		CS	SS		AC	ADDITIONAL MATHS	
WED	CS	ESLA		MATHS	SS		AC LAB A1 -SRG,DG,SKJ MCLAB A2-RS, DR, SH		
THUR	ESLA	SS		AC	MATHS TUT		CS	KANNADA	PLACEMENT
FRI	MATHS TUT	AC		MC	ESLA		ADD MATHS	PROCTORING	
SAT	MCLAB A1-RS, ASR, TT ACLAB A2-SRG, SKJ, PS								

Subject	Subject code	Credits	Course Coordinator
MATHS - 4	18MAT41	3	DR. KARABI SIKDAR
ANALOG CIRCUITS	18EC42	4	DR. SUREKHA R GONDKAR
CONTROL SYSTEMS	18EC43	3	PROF VINUTHA B
ENGINEERING STATISTICS & LINEAR ALGEBRA	18EC44	3	PROF. SHIVRUDRAIAH B
SIGNALS & SYSTEMS	18EC45	3	PROF. HAMSAAVAHINI
MICROCONTROLLER	18EC46	3	DR. C. S. MALA
MICROCONTROLLER LAB	18ECL47	2	DR. ROBIN SINGLA
ANALOG CIRCUITS LAB	18ECL48	2	DR. SUREKHA R GONDKAR
KANNADA	18KAK49/18KVK49	1	

Time Table Officers  
P.P. & V.T.

Chief T.T.O  
Kathin

HOD  
19/04/21

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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

YELAHANKA - BANGALORE - 64

TIME TABLE FOR THE ACADEMIC YEAR 2020-21

Wed : 19/04/2021

SEMESTER : IV  
BRANCH : ECE

SECTION : C  
CLASS ROOM : CR 206  
CLASS TEACHER: DR. ROBIN SINGLA

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 -9.30	9.30 -10.30	10.30 -10.50	10.50 - 11.50	11.50 - 12.50	12.50-1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	ESLA	SS		MATHS	CS		MC	AC LAB C1-DG, GVV, RS MCLAB C2-SH, VB, DR	ADDITIONAL MATHS
TUE	AC	MATHS		SS	CS		MATHS TUT	CS	
WED	CS	SS		AC	MC		MATHS TUT	CS	
THUR							AC	KANNADA	
FRI	MATHS TUT	ESLA		MC	SS		ADD MATHS		PROCTORING
SAT	MC	AC		ESLA	PLACEMENT				

Subject	Subject code	Credits	Course Coordinator
ANALOG CIRCUITS	18EC42	4	DR. DANKAN GOWDA
CONTROL SYSTEMS	18EC43	3	DR. A. SHOBHA RANI
ENGINEERING STATISTICS & LINEAR ALGEBRA	18EC44	3	PROF. RAVINDRA AŞUNDI
SIGNALS & SYSTEMS	18EC45	3	DR. DEEPA N. REDDY
MICROCONTROLLER	18EC46	3	PROF. SHILPA HIREMATH
MICROCONTROLLER LAB	18ECL47	2	PROF. SHILPA HIREMATH
ANALOG CIRCUITS LAB	18ECL48	2	DR. DANKAN GOWDA
KANNADA	18KAK49/18KVK49	1	

Time Table Officers  
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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 YELAHANKA - BANGALORE - 64  
 DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
 TIME TABLE FOR THE ACADEMIC YEAR 2020-21

Wef : 19/04/2021

SEMESTER : VI  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM : CR 207  
 CLASS TEACHER: PROF. JAGANNATH K. B

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30-9.30	9.30-10.30	10.30-10.50	10.50-11.50 11.20-12.10	11.50-12.50	12.50-1.45	1.45-2.40	2.40-3.35	3.35-4.30
MON	OE	ES		PE	DC				
TUE	PE	M&A		OE	DC				
WED	M&A	DC		OE	ES				
THUR	ES	M&A		PLACEMENT					
FRI	DC	ES		M&A	PE				
SAT	ES LAB A2-SKB, SB, AK COMM LAB A1-RN, HVR, CP								

Subject	Subject code	Credits	Course Coordinator
DIGITAL COMMUNICATION	18EC61	4	DR. DANKAN GOWDA
EMBEDDED SYSTEMS	18EC62	4	PROF SURYAKANTH B
MICROWAVE AND ANTENNAS	18EC63	4	DR. RASHMIN
OPERATING SYSTEM	18EC64	4	DR. ANIL KUMAR D/ PROF. SURYAKANTH B
PROFESSIONAL ELECTIVE	DATA STRUCTURES USING C++	3	PROF. JAGANNATH K B/PROF. THYAGARAJ T
OPEN ELECTIVE	18XX65X	3	
EMBEDDED SYSTEMS LABORATORY	18ECL66	2	PROF SURYAKANTH B
COMMUNICATION LABORATORY	18ECL67	2	DR. RASHMIN

Time Table Officers  
 TD 2.17/2

Chief TTD  
 21/4/21

HOD  
 21/4/21

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YELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

TIME TABLE FOR THE ACADEMIC YEAR 2020 - 21

Wed : 19/04/2021

SEMESTER : VI  
BRANCH : ECE

SECTION : B  
CLASS ROOM : CR 307  
CLASS TEACHER: DR. AMIT KUMAR

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	OE	DC		PE	M&A				
TUE	PE	DC		OE	ES				
WED	DC	M&A		OE	ES				
THUR	PLACEMENT	ES		DC	M&A		ES LAB B1-AKD, SKB, JKB COMM LAB B2 HVR, RN, PS		
FRI	ES LAB B2-AKD, SB, SR COMM LAB B1-HVR, AK, LS				PE				
SAT	ES	M&A							

Subject	Subject code	Credits	Course Coordinator
DIGITAL COMMUNICATION	18EC61	4	PROF. HAMSAVAHINI R
EMBEDDED SYSTEMS	18EC62	4	DR. ANIL KUMAR D
MICROWAVE AND ANTENNAS	18EC63	4	DR. AMIT KUMAR
OPERATING SYSTEM	18EC64		DR. ANIL KUMAR D/ PROF. SURYAKANTH B
PROFESSIONAL DATA STRUCTURES ELECTIVE	18EC63	3	PROF. JAGANNATH K B/PROF. THYAGARAJ T
OPEN ELECTIVE	18XX65X	3	
EMBEDDED SYSTEMS LABORATORY	18ECL66	2	DR. ANIL KUMAR D
COMMUNICATION LABORATORY	18ECL67	2	PROF. HAMSAVAHINI R

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Time Table Officers  
NR 2 VVR

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Chief TPO

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HOD

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# BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT

YELAHANKA - BANGALORE - 64

## DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE FOR THE ACADEMIC YEAR 2020-21

Wef : 19/04/2021

SEMESTER : VI  
BRANCH : ECE

SECTION : C  
CLASS ROOM : CR 302  
CLASS TEACHER: PROF. LAXMISAGAR

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30am to 11:50am

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	OE	DC		PE	M&A		ES LAB C1-RVA, JKB, SB COMM LAB C2-IS, MKR, AK		
TUE	PE	ES		OE	M&A				
WED	ES	DC		OE	M&A				
THUR	ES LAB C2-RVA, SR, AK COMM LAB C1-IS, CP, MKR								
FRI	M&A	DC		ES	PE		PROCTORING		
SAT	DC	ES		PLACEMENT					

Subject	Subject code	Credits	Course Coordinator
DIGITAL COMMUNICATION	18EC61	4	PROF. LAXMISAGAR
EMBEDDED SYSTEMS	18EC62	4	PROF. RAVINDRA ASUNDI
MICROWAVE AND ANTENNAS	18EC63	4	DR. RASHMI N
OPERATING SYSTEM	18EC641		DR. ANIL KUMAR D/ PROF. SURYAKANTH B
PROFESSIONAL ELECTIVE	18EC643	3	PROF. JAGANNATH K B/PROF. THYAGARAJ T
OPEN ELECTIVE	18XX65X	3	
EMBEDDED SYSTEMS LABORATORY	18ECL66	2	PROF. RAVINDRA ASUNDI
COMMUNICATION LABORATORY	18ECL67	2	PROF. LAXMISAGAR

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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 YELAHANKA - BANGALORE - 64  
 TIME TABLE FOR THE ACADEMIC YEAR 2020- 21

Wef : 19/04/2021

SEMESTER : VIII  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM : R&D LAB  
 CLASS TEACHER: PROF. SABINA RAHMAN

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 -9.30	9.30 -10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50- 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	PROJECT WORK/INTERNSHIP/SEMINAR								
TUE	PROJECT WORK/INTERNSHIP/SEMINAR								
WED	PROJECT WORK/INTERNSHIP/SEMINAR								
THUR	FON	FON		WLTE	ML/NCS				
FRI	ML/NCS	FON		FON	WLTE				
SAT	WLTE	WLTE		ML/NCS					

Subject	Subject code	Credits	Course Coordinator
WIRELESS CELLULAR AND LTE 4G BROADBAND	17EC81	4	PROF. VINUTHA B
FIBER OPTICS&NETWORKS	17EC82	4	DR. JAYADEVA G.S'
PROFESSIONAL ELECTIVE	17EC834		DR. DEEPA N. REDDY
INTERSHIP PROJECTWORK	17EC835	3	DR. AMBIKA R
SEMINAR	17EC84	2	PROF VINUTHA B/DR. AMIT KUMAR
	17EC85	6	DR. VIJAYALAKSHMI/ PROF. CHANDRAPRABHA
	17EC86	1	PROF. JAGANNATH K B/DR.ROBIN SINGLA

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 DR & VS

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 Chief TTO

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 HOD

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 Dean Academics

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**YELAHANKA - BANGALORE - 64**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

TIME TABLE FOR THE ACADEMIC YEAR 2020-21

Wef : 19/04/2021

SEMESTER : VIII  
 BRANCH : ECE

SECTION : B  
 CLASS ROOM :  
 CLASS TEACHER: DR. PRACHI SHARMA

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	PROJECT WORK/INTERNSHIP/SEMINAR								
TUE	PROJECT WORK/INTERNSHIP/SEMINAR								
WED	PROJECT WORK/INTERNSHIP/SEMINAR								
THUR	WLTE	WLTE		FON	ML/NCS				
FRI	ML/NCS	WLTE		WLTE	FON				
SAT	FON	FON		ML/NCS					

Subject	Subject code	Credits	Course Coordinator
WIRELESS CELLULAR AND LTE 4G BROADBAND	17EC81	4	DR. A. SHOBHA RANI
FIBER OPTICS&NETWORKS	17EC82	4	PROF SABINA RAHMAN
MACHINE LEARNING NETWORK AND CYBER SECURITY	17EC834	3	IDR. DEEPA N. REDDY
PROFESSIONAL ELECTIVE	17EC835	3	PROF. SHILPA HIREMATH
INTERSHIP	17EC84	2	PROF VINUTHA B/DR. AMIT KUMAR
PROJECTWORK	17ECP85	6	DR. VIJAYALAKSHMI/ PROF. CHANDRAPRABHA
SEMINAR	17ECS86	1	PROF. JAGANNATH K B/ DR ROBIN SINGLA

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YELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

TIME TABLE FOR THE ACADEMIC YEAR 2020-21

Wed : 19/04/2021

SEMESTER : VIII  
BRANCH : ECE

SECTION : C  
CLASS ROOM :  
CLASS TEACHER : PROF. MAMATHA K R

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 -9.30	9.30 -10.30	10.30 -10.50	10.50 - 11.50	11.50 - 12.50	12.50-1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30	
MON	PROJECT WORK/INTERNSHIP/SEMINAR									
TUE	PROJECT WORK/INTERNSHIP/SEMINAR									
WED	PROJECT WORK/INTERNSHIP/SEMINAR									
THUR	WLTE	FON		WLTE	ML/NCS					
FRI	ML/NCS	FON		WLTE	WLTE					
SAT	FON	FON		ML/NCS						

Subject	Subject code	Credits	Course Coordinator
WIRELESS CELLULAR AND LTE 4G BROADBAND	17EC81	4	DR ROBIN SINGLA
FIBER OPTICS&NETWORKS	17EC82	4	PROF. JAGANNATH K B
MACHINE LEARNING	17EC834		DR. DEEPA N. REDDY
PROFESSIONAL NETWORK AND CYBER SECURITY	17EC835	3	PROF MAMATHA K R
ELECTIVE	17EC84	2	PROF VINUTHA B/DR. AMIT KUMAR
INTERSHIP	17EC84	2	DR. VIJAY LAKSHMI/PROF. CHANDRAPRABHA
PROJECT WORK	17EC85	6	DR. VIJAY LAKSHMI/PROF. CHANDRAPRABHA
SEMINAR	17EC86	1	PROF. JAGANNATH K B/ DR ROBIN SINGLA

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

TIME TABLE FOR THE PARALLEL/PROMOTED STUDENTS FOR THE ACADEMIC YEAR 2020 - 21  
Wef : 19/04/2021

SEMESTER : VI  
BRANCH : ECE

SECTION : A  
CLASS ROOM : CR 207  
CLASS TEACHER: PROF. JAGANNATH K. B

\*Note-All the morning lab slots are from 8:30 am to 11:30 am. Tea break from 11:30 am to 11:50 am

Sl. No	Subject	Subject Code	Credits	Course coordinator	Lab	Lab In-charge
1	DIGITAL COMMUNICATION	15EC61/17EC61	4	DR. DANKAN GOWDA	CCN	
2	ARM & EMBEDDED SYSTEMS (ES)	15EC62/17EC62	4	PROF SURYAKANTH B		
3	VLSI DESIGN	15EC63/17EC63	4	PROF. SABINA RAHMAN		
4	COMPUTER COMMUNICATION NETWORKS	15EC64/17EC64	4	PROF. THYGARAJ T		
5	PROFESSIONAL ELECTIVE	15EC65/17EC65	3	DR. PRACHI SHARMA		
6	OPEN ELECTIVE	17CV661	3	DR. CHANDRASHEKHARAPPA		
7	EMBEDDED SYSTEMS LABORATORY	15ECL67/17ECL67	2	PROF SURYAKANTH B		
8	COMPUTER NETWORKS LABORATORY	15ECL68/17ECL68	2	PROF THYAGARAJ T		

Sl. No	Subject	Subject Code	Credits	Course coordinator	Lab	Lab In-charge
9	DIGITAL COMMUNICATION	15EC61/17EC61	4	DR. DANKAN GOWDA	CCN	
10	ARM & EMBEDDED SYSTEMS (ES)	15EC62/17EC62	4	PROF SURYAKANTH B		
11	VLSI DESIGN	15EC63/17EC63	4	PROF. SABINA RAHMAN		
12	COMPUTER COMMUNICATION NETWORKS	15EC64/17EC64	4	PROF. THYGARAJ T		
13	PROFESSIONAL ELECTIVE	15EC65/17EC65	3	DR. PRACHI SHARMA		
14	OPEN ELECTIVE	17CV661	3	DR. CHANDRASHEKHARAPPA		
15	EMBEDDED SYSTEMS LABORATORY	15ECL67/17ECL67	2	PROF SURYAKANTH B		
16	COMPUTER NETWORKS LABORATORY	15ECL68/17ECL68	2	PROF THYAGARAJ T		

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**YELAHANKA - BANGALORE - 64**  
**DSD DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**TIME TABLE FOR THE ACADEMIC YEAR 2020 - 21**

Web : 01/09/2020

SEMESTER : III  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM :  
 CLASS TEACHER : Prof. Shashikala J

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	ED	NT		CO	DSD		DSD Lab - A2- AGH, HVR EDI Lab A1- RS, SH		
TUE	DSD Lab - A1- AGH, SC EDI Lab A2- RS, RVA								
WED	DSD	NT		M3	CO		DSD	DIP M3	DIP M3
THUR	PE	M3 - T		DSD	NT		PEI	C <sup>PH</sup>	
FRI	NT	ED		PEI	CO		ED	Placement	
SAT	ED	M3 - T		PEI	CO		ADD MATHS	PROCTORING	

SUBJECT	SUBJECT CODE	CREDI TS	COURSE COORDINATOR
NETWORK THEORY	18EC32	4	PROF. SHASHIKALA J
ELECTRONIC DEVICES	18EC33	4	DR. JAYADEVA G S
DIGITAL SYSTEM DESIGN	18EC34	3	PROF. ASHA G
COMPUTER ORGANIZATION & ARCHITECTURE	18EC35	3	DR. ANIL KUMAR D
POWER ELECTRONICS & INSTRUMENTATION	18EC36	3	PROF. RAHMAN SABINA
ELECTRONIC DEVICES & INSTRUMENTATION LAB	18ECL37	2	PROF. ROBIN SINGLA
DIGITAL SYSTEM DESIGN LAB	18ECL38	2	PROF. ASHA G H
CONSTITUTION OF INDIA	18CPC39	1	PROF. TEJASWINI B J

Time Table Officers  
 AGH& DR

*[Signature]*  
 Chief TPO

*[Signature]*  
 HOD

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 Dean Academics

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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
YELAHANKA - BANGALORE - 64  
**DSD DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
TIME TABLE FOR THE ACADSDMIC YEAR 2020 - 21

Wef : 01/09/2020

SEMESTER : III  
BRANCH : ECE

SECTION : B  
CLASS ROOM :  
CLASS TEACHER : Prof. Laxmisagar H S

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	CO	M-3		PEI	ED		NT	CPH	
TUE	M-3	NT		CO	PEI		ED	DIP Maths	DIP Maths
WED	DSD Lab - B1-AKD, SH EDI Lab B2-SRG,IS				DSD		Placement		
THUR	NT	CO		DSD	M3-T		DSD Lab - B2-AKD, SH EDI Lab B1-SRG,SKJ		
FRI	DSD	ED		M-3	PEI		ADD MATHS		
SAT	NT	ED		PEI	CO				

Subject	Subject code	Credits	Course coordinator
NETWORK THEORY	18EC32	4	PROF. SHASHIKALA J
ELECTRONIC DEVICES	18EC33	4	DR.HANUMANTHARAJU M C
DIGITAL SYSTEM DESIGN	18EC34	3	DR. CS MALA
COMPUTER ORGANIZATION & ARCHITECTURE	18EC35	3	PROF.HAMSAVAHINI
POWER ELECTRONICS & INSTRUMENTATION	18EC36	3	PROF. LAXMISAGAR H S
ELECTRONIC DEVICES & INSTRUMENTATION LAB	18ECL37	2	DR. SUREKHA R G
DIGITAL SYSTEM DESIGN LAB	18ECL38	2	DR. ANIL KUMAR D
CONSTITUTION OF INDIA	18PC39	1	PROF. TEJASWINI B J

Time Table Officers  
ACH& DR

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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**YEELAHANKA - BANGALORE - 64**  
**DSD DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADSDMIC YEAR 2020-21

Wef : 01/09/2020

SEMESTER : III  
 BRANCH : ECE

SECTION : C  
 CLASS ROOM :  
 CLASS TEACHER : Dr.Dankan Gowda V

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	ED	CO		DSD	M-3		NT	PEI	
TUE	DSD	ED		M-3	PEI		CO	DIP Maths	DIP Maths
WED	CO	M3-T		ED	DSD-C		DSD Lab - C2- HVR,AGH ED1 Lab C1- DG,SKJ		
THUR	PEI	M3-T		NT-C	ED		CPH	Placement	
FRI	DSD Lab - C1- HVR,AKD ED1 Lab C2- DG,RVA						ADD MATHS		
SAT	PEI-C	DSD		CO	NT-C				

SUBJECT	SUBJECT CODE	CREDITS	COURSE COORDINATOR
NETWORK THEORY	18EC32	4	DR.DANKAN GOWDA V
ELECTRONIC DEVICES	18EC33	4	DR.HANUMANTHARAJU M C
DIGITAL SYSTEM DESIGN	18EC34	3	PROF.HAMSAVAHINI
COMPUTER ORGANIZATION & ARCHITECTURE	18EC35	3	PROF.RAVINDRA V ASUNDI
POWER ELECTRONICS & INSTRUMENTATION	18EC36	3	PROF.HAMSAVAHINI
ELECTRONIC DEVICES & INSTRUMENTATION LAB	18EC137	2	PROF.CHANDRAPRABHA
DIGITAL SYSTEM DESIGN LAB	18EC138	2	DR.DANKAN GOWDA V
CONSTITUTION OF INDIA	18CP39	1	PROF. TEJASWINI B J

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**YELAHANKA - BANGALORE - 64**  
**DSD DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**TIME TABLE FOR THE ACADSDMIC YEAR 2020-21**

Wef : 01/09/2020

SEMESTER : V  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM :  
 CLASS TEACHER : Prof. Jagganatha K B

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	DSP	PCS		TIME-A	EMW		VHDL		
TUE	EMW	ITC		DSP	PCS			DSP Lab - A1-SCT, DG HDL LAB - A2 - JKB, SR	
WED			DSP Lab - A2-SCT, VB HDL LAB - A1 - JKB, SC		EMW		ITC	EVS	
THUR	ITC	DSP		PCS	VHDL		TIME	Placement	
FRI	ITC	VHDL		TIME	EMW				
SAT	TIME	PCS		VHDL	DSP				

SUBJECT	SUBJECT CODE	CREDITS	COURSE COORDINATOR
MANAGEMENT AND ENTERPRSHIP	18ES51	3	PROF. THYAGARAJ T
DIGITAL SIGNAL PROCESSING	18EC52	4	DR. VIJAYALAKSHMI V
PRINCIPLES OF COMMUNICATTION SYSTEMS	18EC53	4	DR. SHANTHI CHANDA
INFORMATION THEORY AND CODNG	18EC54	3	DR. AMBIKA R
ELECTROMAGNETIC WAVES	18EC55	3	PROF. SANESH C T
VERILOG HDL	18EC56	3	PROF. JAGANNATH K B
DSP LAB	18ECL57	2	PROF. SANESH C T
HDL LAB	18ECL58	2	PROF. JAGANNATH K B
ENVINROMENTAL STUDIES	18CIV59	1	PROF.ROBIN SINGLA

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YELAHANKA - BANGALORE - 64

DSDPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE FOR THE ACADSDMIC YEAR 2020 - 21

Wef : 01/09/2020

SEMESTER : V  
BRANCH : ECE

SECTION : B  
CLASS ROOM :  
CLASS TEACHER : Prof. Suryakanth B

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 -9.30	9.30 -10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12 50	12 50- 1 45	1 45 - 2. 40	2.40 - 3.35	3.35 - 4.30
MON	VHDL	EMW		DSP	ITC		TIME		
TUE	VHDL	EMW		TIME	PCS		ITC	VHDL	
WED	EMW	DSP		PCS	TIME			DSP Lab - B2- DR, MKR HDL LAB - B1 - SKB, TT	
THUR	DSP	PCS		TIME	VHDL		EMW		
FRI	PCS	DSP		ITC	EVS				
SAT	DSP Lab - B1- DR, VB HDL LAB - B2 - SKB, RVA								

SUBJECT	SUBJECT CODE	CREDITS	COURSE COORDINATOR
MANAGEMENT AND ENTERPERSHIP	18ES51	3	PROF.RAVINDRA ASUNDI
DIGITAL SIGNAL PROCESSING	18EC52	4	DR.DEEPA REDDY
PRINCIPLES OF COMMUNICATION SYSTEMS	18EC53	4	PROF.ASHA G H
INFORMATION THEORY AND CODING	18EC54	3	PROF.SHIVARUDRAIAH B
ELECTROMAGNETIC WAVES	18EC55	3	DR.DANKAN GOWDAV
VERILOG HDL	18EC56	3	PROF.SURYAKANTH B
DSP LAB	18EC157	2	PROF.SURYAKANTH B
HDL LAB	18EC158	2	DR.DEEPA REDDY
ENVIRONMENTAL STUDIES	18CIV59	1	PROF.ROBIN SINGLA

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AGH& DR

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**YELAHANKA - BANGALORE - 64**  
**DSD DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**TIME TABLE FOR THE ACADSDMIC YEAR 2020-21**

Wef : 01/09/2020

SEMESTER : V  
 BRANCH : ECE

SECTION : C  
 CLASS ROOM :  
 CLASS TEACHER : Prof. Sabina R

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

SAT	TIME	HDL	EMW	DSP
	8.30-9.30	9.30-10.30	10.30-10.50	10.50-11.50
MON	EMW	VHDL	ITC	PCS
TUE	DSP	VHDL	ITC	EMW
WED	TIME	DSP	PCS	VHDL
THUR	PCS	ITC	EMW	TIME
FRI	DSP Lab - C1- ASR,VB HDL LAB - C2 - TT,SB			
			EMW	DSP
				12.50-1.45
				1.45-2.40
				2.40-3.35
				3.35-4.30
				DSP Lab - C2- ASR, DR HDL LAB - C1 - TT, SKB
				PCS
				Placement
				DSP

SUBJECT	SUBJECT CODE	CREDITS	COURSE COORDINATOR
MANAGEMENT AND ENTERPRENSHIP	18ES51	3	DRANIL KUMAR D
DIGITAL SIGNAL PROCESSING	18EC52	4	DRA SHOBHARANI
PRINCIPLES OF COMMUNICATION SYSTEMS	18EC53	4	DR.SHANTHICHANDRA
INFORMATION THEORY AND CODING	18EC54	3	PROF.SURYAKANTH B
ELECTROMAGNETIC WAVES	18EC55	3	PROF.SANDESH CT
VERILOG HDL	18EC56	3	PROF.SABINA R
DSP LAB	18EC57	2	PROF.THYAGARAJ T
HDL LAB	18EC58	2	DRA SHOBHARANI
ENVIRONMENTAL STUDIES	18CIV59	1	PROF.ROBIN SINGLA

Time Table Officers

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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**YELAHANKA - BANGALORE - 64**  
**DSD DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**TIME TABLE FOR THE ACADSDMIC YEAR 2020 - 21**

Wef : 01/09/2020

SEMESTER : VII  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM :  
 CLASS TEACHER : Prof. Rashmi N

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	MWA	SAT.COM		PE	DIP				
TUE	PE	MWA		Placement					
WED	MWA	PE		DIP	RTS/CRYPTPO				
THUR	RTS/CRYPTPO	DIP		PE	SATCOM/IOT				
FRI	ADC Lab - A1- GVV/CP VLSI LAB - A2 - LS,SC								
SAT	DIP	RTS/CRYPTPO		PROJECT					

SUBJECT	SUBJECT CODE	CREDITS	COURSE COORDINATOR
MICROWAVE & ANTENNA	17EC71	4	PROF. RASHMI N
DIGITAL IMAGE PROCESSING	17EC72	4	PROF. SUREKHA G
POWER ELECTRONICS	17EC73	4	DR. JAYADEVA G S
REAL TIME SYSTEMS	17EC743	3	PROF. LAXMISAGAR/SHIVARUDRAIH
CRYPTOGRAPHY	17EC744	3	PROF. MAMATHA/JAGANNATH
SAT.COM	17EC755	3	DR. ASHOBHARANI/ DR.DEEPAREDDY/SHILPA H
IOT	17EC752	3	PROF. VINUTHA B
ADVANCED COMMUNICATION LAB	17ECL76	2	PROF.CHANDRAPRABHA
VLSI LAB	17ECL77	2	PROF.SABINA R

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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**YELAHANKA - BANGALORE - 64**  
**DSDPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**TIME TABLE FOR THE ACADSDMIC YEAR 2020 - 21**

Wef : 01/09/2020

SEMESTER : VII  
 BRANCH : ECE

SECTION : B  
 CLASS ROOM :  
 CLASS TEACHER : Prof. SHIPLA H

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	PE	SATCOM/IOT		DIP	MWA		ADC Lab B2 - MKR,RN VLSI LAB - B1 - SB,SC		
TUE		ADC Lab - B1 - MKR,SKJ VLSI LAB - B2 - SB,JKB			MWA				
WED	DIP	PROJECT		PE	RTS/CRYPTPO				
THUR	RTS/CRYPTPO	PE		MWA	SATCOM/IOT				
FRI	MWA-B	DIP		PROJECT					
SAT	PE	RTS/CRYPTPO		DIP	PLACEMENT				

SUBJECT	SUBJECT CODE	CREDITS	COURSE COORDINATOR
MICROWAVE & ANTENNA	17ECT1	4	PROF. RASHMI N
DIGITAL IMAGE PROCESSING	17ECT2	4	PROF. MAMATHA K R
POWER ELECTRONICS	17ECT3	4	PROF. ROBIN SINGLA
REAL TIME SYSTEMS	17ECT43	3	PROF. LAXMISAGAR/SHIVARUDRAIH
CRYPTOGRAPHY	17ECT44	3	PROF. MAMATHA/JAGANNATH
SAT.COM	17ECT55	3	DR. A SHOBBHARANI/ DR. DEEPA REDDY/SHILPA H
IOT	17ECT52	3	PROF. VINUTHA B
ADVANCED COMMUNICATION LAB	17ECL76	2	PROF. MAMATHA K R
VLSI LAB	17ECL77	2	PROF. SHIVARUDRAIH B

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**DSDPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
**TIME TABLE FOR THE ACADSDMIC YEAR 2020 - 21**

Wef : 01/09/2020

SEMESTER : VII  
 BRANCH : ECE

SECTION : C  
 CLASS ROOM :  
 CLASS TEACHER : Prof. VINUTHA B

\*Note-All the morning lab slots are from 8:30 am to 11:30 am.

	8.30 -9.30	9.30 -10.30	10.30 -10.50	10.50 - 11.50	11.50 - 12.50	12.50-1 45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	PE	SATCOM/IOT		MWA	DIP				
TUE	DIP	PE-C		MWA	PLACEMENT		SATCOM/IOT		
WED	ADC Lab - C1-CP,DG VLSI LAB - C2 - SR,SC			ADC Lab - C1-CP,DG VLSI LAB - C2 - SR,SC	RTS/CRYPTPO				
THUR	RTS/CRYPTPO	MWA		PE	SATCOM/IOT		ADC Lab - C2- CP,RN VLSI LAB - C1 - SR,RS		
FRI	MWA	DIP		PROJECT					
SAT	DIP	RTS/CRYPTPO		PE	PLACEMENT				

Subject	Subject code	Credits	Course coordinator
Microwave & Antenna	17EC71	4	Prof. Vinutha B
Digital Image Processing	17EC72	4	Prof. Shiipa H
Power Electronics	17EC73	4	Prof. Thyagaraj T
Real time Systems	17EC743	3	Prof. Laxmisagar/Shivarudraih
Cryptography	17EC744	3	Prof. Mamatha/Jaganath
Sat.Com	17EC755	3	Dr. A Shobharani/ Dr. Deepareddy/Shiipa H
IOT	17EC752	3	Prof. Vinutha B
Advanced communication Lab	17ECL76	2	Prof.Chandraprabha
VLSI Lab	17ECL77	2	Prof. Sabina R

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YELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

TIME TABLE FOR THE ACADEMIC YEAR 2019-20

Wef : 10/02/2020

SEMESTER : IV  
BRANCH : ECE

SECTION : A  
CLASS ROOM : CR 105  
CLASS TEACHER : PROF HAMSAVAHINI

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	CS	ESLA		SS	M4		MC	PLACEMENT	KANNADA
TUE	MC	M4		AE	CS		E-SHISKSHA	Dip Maths	
WED	ESLA	AE		SS	MC		AE-LAB -- A1-SH,TT,DG MC LAB -A3,A4 --RB, HVR,VB		
THUR	ESLA	SS		MC	M4		AE	CS	
FRI	SS	M4		AE	CS		PROCTORING		
SAT	MC-A1,A2 RB,HVR,VB AE-A3-SRGS,SH A4-SKJ,SC ESLA								

Subject	Subject code	Credits	Course coordinator
MATHS-4	18MAT41	04	DR.JOY JOSEPH
ANALOG CIRCUITS	18EC42	04	DR.SUREKHA G
CONTROL SYSTEMS	18EC43	03	PROF. VINUTHA B
ESLA	18EC44	03	PROF. RAVINDRA A
SIGNALS AND SYSTEMS	18EC45	03	PROF. HAMSAVAHINI R
MICROCONTROLLER	18EC46	03	PROF. SANEESH C T

Subject	Subject code	Credits	Course coordinator
MICROCONTROLLER LAB	18ECL47	02	DR.ROBIN
ANALOG CIRCUITS LAB	18ECL48	02	DR.SUREKHA G

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VELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
TIME TABLE FOR THE ACADEMIC YEAR 2019 - 20

SEMESTER : IV  
BRANCH : ECE

Laboratory batches:

SECTION : B  
CLASS ROOM : CR 205  
CLASS TEACHER : PROF. CHANDRAPRABHA

Wef : 10/02/2020

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	M4	SS		AE	MC		CS	E-SHISKSHA	3.35 - 4.30
TUE	CS	ESLA		MC	SS		M4	Dip Maths	
WED	AE	M4		SS	CS		ESLA	PLACEMENT	KANNADA
THUR	MC	AE		ESLA	SS		AE LAB -B1-CP,JGS MC LAB B3,B4-SCT,RB,VB		
FRI	AE LAB -B3-CP,DG MC LAB B1,B2-SCT,HVR,VB	B4-SH,SC			M4				
SAT	ESLA	MC		CS	AE				

Subject	Subject code	Credits	Course coordinator
MATHS 4	18MAT41	04	DR. SHIVAPRASAD
ANALOG CIRCUITS	18EC42	04	DR. HANUMANTHAR AIJ
CONTROL SYSTEMS	18EC43	03	PROF. CHANDRAPRABHA
ESLA	18EC44	03	PROF. ASHA G HAGARGUND
SIGNALS AND SYSTEMS	18EC45	03	DR. DEEPA REDDY
MICROCONTROLLER	18EC46	03	PROF. SANEESH C T

MICROCONTROLLER LAB	18ECL47	02	PROF. SANEESH C T
ANALOG CIRCUITS LAB	18ECL48	02	PROF. CHANDRAPRABHA

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
TIME TABLE FOR THE ACADEMIC YEAR 2019-20

Wef : 10/02/2020

SEMESTER : IV  
BRANCH : ECE

SECTION : C

CLASS ROOM : CR 206

CLASS TEACHER : PROF SHASHIKALA J

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50 11.20 - 12.10	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	CS	AE		M4	MC		ESLA	SS	E-SHISKSHA
TUE	AE LAB -C1-GVV,SKI C2-SR,SH MC LAB C3,C4 -RB,DR,TT,ASR				M4		SS	DIP MATHS	
WED	AE LAB -C3-GVV,CP C4-AR,SC MC LAB C1,C2 -TT,IS,ASR				ESLA		CS	AE	
THUR	SS	M4		ESLA	CS		MC	KANNADA	PLACEMENT
FRI	M4	AE		SS	MC		PROCTORING		
SAT	AE	CS		MC	ESLA				

Subject	Subject code	Credits	Course coordinator
MATHS 4	18MAT41	04	DR. ANNAPPOORNA
ANALOG CIRCUITS	18EC42	04	DR. HANUMANTHARAJ U
CONTROL SYSTEMS	18EC43	04	DR. SHOBHA RANI
ESLA	18EC44	04	PROF. SHASHIKALA J
SIGNALS AND SYSTEMS	18EC45	04	DR. VIJAYALAXMI G V
MICROCONTROLLER	18EC46	04	DR. C.S. MALA

MICROCONTROLLER LAB	18ECL47	PROF. THYAGARAJAN
ANALOG CIRCUITS LAB	18ECL48	DR. VIJAYALAXMI G V

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**MS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 YELAHANKA - BANGALORE - 64  
 TIME TABLE FOR THE ACADEMIC YEAR 2019-20

SEMESTER : VI  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM : CR 302

CLASS TEACHER: PROF. MAMATHA K R

Wef : 10/02/2020

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50 11.20 - 12.10	11.50 - 12.50	12.50 - 1	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	OPEN ELECTIVE	ARM		CCN	PROFESSIONAL ELECTIVE	45	VLSI		
TUE	CCN	OPEN ELECTIVE		ARM	DC		CCN LAB A1 RVA, CP, SN		
WED	DC	VLSI		CCN	ARM		EMBEDDED CONTROLLER LAB, A2-SBM, RN, SB		
THUR	PROFESSIONAL ELECTIVE	ARM		OPEN ELECTIVE	VLSI		PROFESSIONAL ELECTIVE	E-SHISKSHA	
FRI	DC	CCN		VLSI	PLACEMENT		DC		
SAT	CCN LAB A1 RVA, MKR, AGH EMBEDDED CONTROLLER LAB A2-SB, AKD, TT, JS				INDUSTRIAL TRAINING		PROCTORING		

Subject	Subject code	Credits	Course coordinator
DIGITAL COMMUNICATION	17EC61	04	PROF. RASHMIN
ARM MICROCONTROLLER & EMBEDDED SYSTEMS	17EC62	04	PROF. SURYAKANT B M
VLSI DESIGN	17EC63	04	PROF. SABINA R
COMPUTER COMMUNICATION NETWORKS	17EC64	04	PROF. MAMATHA K R

ARTIFICIAL NEURAL NETWORKS	17EC653	03	PROF. RASHMIN
DIGITAL SWITCHING SYSTEMS	17EC663	3	DRANIL KUMAR D / PROF. SHILPA H
EMBEDDED CONTROLLER LAB	17ECL67	2	PROF. SHIVARUDRAI AH B
COMPUTER NETWORKS LAB	17ECL68	2	PROF. RAVINDRA ASUNDI
DATA STRUCTURES USING C++	17EC661	3	PROF. LAXMISAGAR HS
TOTAL QUALITY MANAGEMENT	17ME664	3	PROF. THYAGARAJ T

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**MS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 YELAHANKA - BANGALORE - 64  
 TIME TABLE FOR THE ACADEMIC YEAR 2019-20

Web : 10/02/2020

SEMESTER : VI  
 BRANCH : ECE

SECTION : B  
 CLASS ROOM : CR 307  
 CLASS TEACHER : SHANTHI CHANDRA

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	OPEN ELECTIVE	VLSI DESIGN		ARM	PROFESSIONAL ELECTIVE		CCN		
TUE	ARM	OPEN ELECTIVE		CCN	VLSI DESIGN		DC		
WED	ARM	CCN		VLSI DESIGN	DC		PROFESSIONAL ELECTIVE	E-SHISKSHA	
THUR	PROFESSIONAL ELECTIVE	DC		OPEN ELECTIVE	CCN		CCN LAB B1 MKR,HVR,DR EMBEDDED CONTROLLER LAB B2 -SBM,SR,LS		
FRI	CCN LAB B2 MKR,RVA,AGH,DR EMBEDDED CONTROLLER LAB B1 -SBM,RN,LS				PLACEMENT		PROCTORING		
SAT	VLSI DESIGN	ARM		DC	INDUSTRIAL TRAINING				

	Subject	Subject code	Credits	Course coordinator
	DIGITAL COMMUNICATION	17EC61	04	DR.DANKAN GOWDA
	ARM MICROCONTROLLER & EMBEDDED SYSTEMS	17EC62	04	PROF.SURYAKANTH B M
	VLSI DESIGN	17EC63	04	DR.JAYADEVA G S
	COMPUTER COMMUNICATION NETWORKS	17EC64	04	PROF.MAMATHA K R
	ARTIFICIAL NEURAL NETWORKS	17EC653	03	PROF RASHMIN

	TOTAL QUALITY MANAGEMENT	17ME664	3	PROF.THAYAGARAJ T
	DIGITAL SWITCHING SYSTEMS	17EC663	3	DR.ANIL/PROF.SHILPA H
	EMBEDDED CONTROLLER LAB	17ECL67	2	PROF. SURYAKANTH B M
	COMPUTER NETWORKS LAB	17ECL68	2	PROF.MAMATHA K R
	DATA STRUCTURES USING C++	17EC661	3	PROF.LAXMISAGAR H S

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

YELAHANKA - BANGALORE - 64  
 TIME TABLE FOR THE ACADEMIC YEAR 2019-20

Wef : 10/02/2020

SEMESTER : VI  
 BRANCH : ECE

SECTION : C  
 CLASS ROOM : CR 207  
 CLASS TEACHER : DR. DEEPA REDDY

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	OPEN ELECTIVE	ARM		VLSI	PROFESSIONAL ELECTIVE		CCN LAB C1-AGH,RVA,RN EMBEDDED CONTROLLER LAB C2 - AKD,SBM,TT		
TUE	VLSI	OPEN ELECTIVE		ARM	DC				
WED	CCN LAB C2-AGH,DR,SN EMBEDDED CONTROLLER LAB C1 -AKD,SB,SR								
THUR	PROFESSIONAL ELECTIVE	DC		OPEN ELECTIVE	CCN		PROFESSIONAL ELECTIVE	CCN	
FRI	CCN	ARM		VLSI	DC		ARM	*E-SHISKSHA	
SAT	CCN	DC		PLACEMENT	INDUSTRIAL TRAINING		PROCTORING		

Subject	Subject code	Credits	Course coordinator
DIGITAL COMMUNICATION	17EC61	04	PROF.LAXMISAGAR HS
ARM MICROCONTROLLER & EMBEDDED SYSTEMS	17EC62	04	DR.ANIL KUMAR D
VLSI DESIGN	17EC63	04	PROF. SHIVARUDRAIAHB
COMPUTER COMMUNICATION NETWORKS	17EC64	04	DR.SANTHI NATARAJAN
ARTIFICIAL NEURAL NETWORKS	17EC653	03	PROF. RASHMIN

TOTAL QUALITY MANAGEMENT	DIGITAL SWITCHING SYSTEMS	EMBEDDED CONTROLLER LAB	COMPUTER NETWORKS LAB	DATA STRUCTURES USING C++
17ME664	17EC663	17ECL67	17ECL68	17EC661
3	3	2	2	3
PROF. THYAGARAJ T	DRANIL/PROF. SHILPA H	DR. ANIL KUMAR D	PROF. ASHAG HAGARGUND	PROF.LAXMISAGAR HS

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**MS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
VELAHANKA - BANGALORE - 64  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
TIME TABLE FOR THE ACADEMIC YEAR 2019 - 20

Wed : 10/02/2020

SEMESTER : VIII  
BRANCH : ECE  
SECTION : A  
CLASS ROOM : SATURDAY 8:30 TO 11:50 CR-105, 11:50 TO 12:50 R&D LAB, FRIDAY 8:30 TO 11:50 CR-205, 11:50 TO 4:30 CR-302  
CLASS TEACHER : PROF THYAGARAJ T

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50 11.20 - 12.10	11.50 - 12.50	12.50 - 1 45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	PROJECT WORK/INTERNSHIP								
TUE	PROJECT WORK/INTERNSHIP								
WED	PROJECT WORK/INTERNSHIP								
THUR	PROJECT WORK/INTERNSHIP								
FRI	FOC	LTE 4G		ML/NCS	LTE 4G		FOC	ML/NCS	LTE 4G
SAT	ML/NCS	FOC		FOC	LTE 4G				

Subject	Subject code	Course coordinator
WIRELESS CELLULAR AND LTE 4G BROADBAND	15EC81	DR PROF.ROBIN
FIBER OPTICS AND NETWORKS	15EC82	PROF. SABINA RAHMAN
NETWORK AND CYBER SECURITY	15EC835	DR. AMBIKA R

  
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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 YELAHANKA - BANGALORE - 64  
 TIME TABLE FOR THE ACADEMIC YEAR 2019 - 20

Wef : 10/02/2020

SEMESTER : VIII SECTION : B  
 BRANCH : ECE CLASS ROOM : FRIDAY:CR 307, SATURDAY:CR 302  
 CLASS TEACHER : PROF SHILPA HIREMATH

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50 11.20 - 12.10	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	PROJECT WORK/INTERNSHIP								
TUE	PROJECT WORK/INTERNSHIP								
WED	PROJECT WORK/INTERNSHIP								
THUR	PROJECT WORK/INTERNSHIP								
FRI	FOC	LTE4G		ML/NCS	LTE4G		FOC	ML/NCS	4G LTE
SAT	ML/NCS	FOC		LTE4G	FOC				
	Subject		Subject code	Course coordinator					
	WIRELESS CELLULAR AND LTE 4G BROADBAND		15EC81	DR. A.SHOBHA RANI					
	FIBER OPTICS AND NETWORKS		15EC82	PROF. SHIVARUDRAIAH					
	NETWORK AND CYBER SECURITY		15EC835	PROF. SHILPA HIREMATH					

*[Signature]*  
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 Chief TTD

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 HOD

*[Signature]*  
 Dean Academics

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SEMESTER : III  
BRANCH : ECE

Laboratory batches:

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING  
TIME TABLE FOR THE ACADEMIC YEAR 2019 - 20

Wed 12/07/2019

SECTION : IA  
CLASS ROOM : CR 105  
CLASS TEACHER: PROF RASHMIN

Day	Time	Subject	Code	Credits	Coordinator
MON	8.30 - 9.30	ED	M3	04	
MON	9.30 - 10.30	M3		04	
MON	10.30 - 10.50				
MON	10.50 - 11.50	CO		04	
MON	11.20 - 12.10				
MON	11.50 - 12.50	PR		04	
MON	12.50 - 1.45				
MON	1.45 - 2.40	DSD		04	
MON	2.40 - 3.35	DIP M3		04	
MON	3.35 - 4.30	DIP M3		04	
TUE					
WED					
THUR					
FRI					
SAT					

Subject	Subject code	Credits	Course coordinator
ELECTRONIC DEVICES	18EC32	04	DR. JAYADEVA
DIGITAL SYSTEM DESIGN	18EC33	04	PROF. HANSAVAHINI
NETWORK THEORY	18EC34	04	PROF. SABINA RAHAMAN
COMPUTER ORGANIZATION	18EC35	04	PROF. RASHMIN
POWER ELECTRONICS	18EC36	04	PROF. CHANDRAPRABHA

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 YELAHANKA - BANGALORE - 64  
 TIME TABLE FOR THE ACADEMIC YEAR 2019-20

Wef : 29/07/2019

SEMESTER : III  
 BRANCH : ECE

SECTION : B  
 CLASS ROOM : CR 205  
 CLASS TEACHER : PROF LAXMISAGAR

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50 11.20 - 12.10	11.50 - 12.50	12.50 - 1	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	CO	M 3		PE	CPH		NT	PLACEMENT	
TUE	DSD	NT		M 3	PE		ED	DIP M3	DIP M3
WED	← DE LAB B1 ED LAB B2 →				CO		DSD	ED	
THUR	M 3	CO		DSD	NT		DE LAB B2 ED LAB B1		
FRI	PE	CO		ED	DSD		M 3	PROCTORING	
SAT	ED	NT		M 3	PE				

Subject	Subject code	Credits	Course coordinator
ELECTRONIC DEVICES	18EC32	04	DR.HANUMANTHARAJU
DIGITAL SYSTEM DESIGN	18EC33	04	PROF. ASHA G H
NETWORK THEORY	18EC34	04	PROF. SHASHIKALA J
COMPUTER ORGANIZATION	18EC35	04	PROF. HAMSAVAHINI R
POWER ELECTRONICS	18EC36	04	PROF. LAXMISAGAR

*[Signature]*  
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**BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT**  
**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 YELAHANKA - BANGALORE - 64  
 TIME TABLE FOR THE ACADEMIC YEAR 2019-20

Wed : 29/07/2019

SEMESTER : III  
 BRANCH : ECE

SECTION : C  
 CLASS ROOM : CR206  
 CLASS TEACHER : PROF CHANDRAPRABHA

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50 11.20 - 12.10	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	ED	CO		DSD	NT		M3	PE	
TUE	CO	ED		DSD	PE		NT	DIP M3	DIP M3
WED	M3	PE		ED	DSD		CPH	PLACEMENT	
THUR	CO	ED		M3	PE		LD C2 / AEC C1 AKD/RVA		
FRI	← C1 LD AKD C2AEC RVA →				DSD		DIP M3	PROCTORING	
SAT	PE	NT		M3	CO				

Subject	Subject code	Credits	Course coordinator
ELECTRONIC DEVICES	18EC32	04	DR.HANUMANTHARAJU
DIGITAL SYSTEM DESIGN	18EC33	04	SANEESH C T
NETWORK THEORY	18EC34	04	SABINA RAHAMAN
COMPUTER ORGANIZATION	18EC35	04	RAVINDRA ASUNDI
POWER ELECTRONICS	18EC36	04	CHANDRAPRABHA

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 YELAHANKA - BANGALORE - 64  
 TIME TABLE FOR THE ACADEMIC YEAR 2019-20

SEMESTER : V  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM : CR 302  
 CLASS TEACHER : PROF SURYAKANT B M

Wef : 29/07/2019

Day	Time Slot	Subject	Subject code	Credits	Course coordinator	Subject	Subject code	Credits	Course coordinator		
MON	8.30 - 9.30	OPEN ELECTIVE	HDL	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1	145 - 2.40	2.40 - 3.35	3.35 - 4.30	
		DSP	OPEN ELECTIVE								DSP
TUE	DSP	OPEN ELECTIVE									
WED	DSP LAB A2 HDL LAB A1										
THUR	HDL	ITC			OPEN ELECTIVE	DSP					
FRI	ITC	HDL			M & E	MSP430/OS			PROCTORING		
SAT	M & E	DSP			ITC	PLACEMENT					
MANAGEMENT AND ENTREPRENEURSHIP	17ESS1	04	PROF. THYAGARA J T/	OPERATING SYSTEM /MSP 430	17ESS53/ 17ESS55	03	ANIL KUMAR D/VUAYLAKSHMI/SURYAKANT				
								DIGITAL SIGNAL PROCESSING	17EC52	04	PROF. SANEESH C T
								VERILOG HDL	17EC53	04	PROF. SURYAKANT B
INFORMATION THEORY AND CODING	17EC54	04	DR. AMBIKA R								

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 TIME TABLE FOR THE ACADEMIC YEAR 2019-20

Wef : 29/07/2019

SEMESTER : V  
 BRANCH : ECE

SECTION : B  
 CLASS ROOM : CR 307  
 CLASS TEACHER : PROF JAGANNATH K B

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50 11.20 - 12.10	11.50 - 12 50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	OPEN ELECTIVE	ITC		DSP	ME		PLACEMENT	MSP430/OS	
TUE	ITC	OPEN ELECTIVE		ME	VHDL				
WED	VHDL	ME		DSP	MSP430/OS			HDL LAB-B2 DSP LAB-B1	
THUR	DSP	VHDL		OPEN ELECTIVE	ME				
FRI	DSP	VHDL		ITC	MSP430/OS			PROCTORING	
SAT		HDL LAB-B2 DSP LAB-B1			ITC				
	Subject	Subject code	Credits	Course coordinator	Subject	Subject code	Credits	Course coordinator	
	MANAGEMENT AND ENTREPRENEURSHIP	17ES51	04	PROF. RA VINNDRA ASUNDI	OPERATING SYSTEM /MSP 430	17ES553/1 7ES555	03	ANIL KUMAR D/VIJAYLAKSHMI/ SURYAKANT	
	DIGITAL SIGNAL PROCESSING	17EC52	04	DR. SHOBHA RANI	AUTOMATIVE ELECTRONICS/OOPS USING C++	17ES561/1 7EC562	03	THYAGARAJ T/JAGANNATH	
	VERILOG HDL	17EC53	04	PROF. JAGANNATH K B					
	INFORMATION THEORY AND CODING	17EC54	04	PROF. VINUTHA B					

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 YELAHANKA - BANGALORE - 64  
 TIME TABLE FOR THE ACADEMIC YEAR 2019-20

SEMESTER : V  
 BRANCH : ECE

Wef : 29/07/2019

SECTION : C  
 CLASS ROOM : CR 207  
 CLASS TEACHER: PROF SHIVARUDRAIAH B

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50 11.20 - 12.10	11.50 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	OPEN ELECTIVE	DSP		ME	ITC			C2 HDL LAB C1 DSP LAB	
TUE	VHDL	OPEN ELECTIVE		ITC	DSP				
WED	DSP	ME		HDL	MSP430/OS		PLACEMENT		
THUR	ME	ITC		OPEN ELECTIVE	HDL		DSP		
FRI	← C1 HDL LAB C2 DSP LAB →				MSP430/OS				PROCTORING
SAT	ME	ITC		HDL	MSP430				

Subject	Subject code	Credits	Course coordinator	Subject	Subject code	Credits	Course coordinator
MANAGEMENT AND ENTREPRENEURSHIP	17ESS1	04	PROF. ANIL KUMAR D	OPERATING SYSTEM /MSP 430	17ESS53/ 17ESS55	03	ANIL KUMAR D/VIJAYLAKSH M/SURYAKANT
DIGITAL SIGNAL PROCESSING	17EC52	04	PROF. DEEPA REDDY	AUTOMATIVE ELECTRONICS/OOPS USING C++	17ESS61/ 17EC562	03	THYAGARAJ T/JAGANNATH
VERILOG HDL	17EC53	04	PROF. SHIVARUD RAIHAH B				
INFORMATION THEORY AND CODING	17EC54	04	PROF. ASHA GH				

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 TIME TABLE FOR THE ACADEMIC YEAR 2019-20

SEMESTER : VII  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM :  
 CLASS TEACHER : PROF VINUTHA B

Wef : 29/07/2019

	8.30 - 9.30	9.30 - 10.30	10.30 - 10.50	10.50 - 11.50	11.50 - 12.50	12.50 - 1	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
MON	↔ VLSI-A2 LAB, ASD A1-LAB			↔	MWR1		DIP		
TUE	↔ VLSI-A1 LAB, ASD A2-LAB			↔			SATCOMM		
WED	RTS/CRYPTO	MWR		PE					
THUR	RTS/CRYPTO	DIP		PROJECT WORK			RTS/CRYPTO		
FRI	PE	MWR		DIP	PROJECT WORK				
SAT	MWR	SATCOMM		PE	PROJECT WORK				

Subject	Subject code	Course coordinator
MICROWAVE AND RADAR	15EC71	PROF. VINUTHA B
DIGITAL IMAGE PROCESSING	15EC72	PROF. MAMATHA K R
POWER ELECTRONICS	15EC73	PROF. DR. JAYADEVA
RTS/CRYPTOGRAPHY	15EC743/744	PROF. SHIVARUDRAIAH B/ MAMATHA K R
SATELLITE COMMUNICATION	15EC755	DR. ASHOBHA RANI

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

Wef : 1/02/2018

SEMESTER : IV  
 BRANCH : ECE

SECTION : A  
 CLASS ROOM : CR 406  
 CLASS TEACHER : PROF THYAGARAJ T

**Laboratory batches:**

A11:1BY16EC001 TO 1BY16EC017 (14)	A12: 1BY16EC018 TO 1BY16EC032 (14)	A21: 1BY16EC033 TO 1BY16EC046 (14)	A22: 1BY16EC047 TO 1BY15EC026 (15)
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	8.30 - 9.20	9.20 - 10.10	10.10 - 10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 1.00	1.00 - 2.00	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
MON	PCS	LIC	TEA BREAK	LIC LAB/ MP LAB			LUNCH BREAK	MP	CO-CURRICULAR ACTIVITY	
TUE	SS	M4		LIC LAB/ MP LAB				PCS	DIP MATHS	
WED	MP	LIC		SS	CS	PCS		TUTORIAL	SPORTS/CULTURAL	
THUR	CS	M4		MP	SS	LIC		M4 TUTORIAL	PCS	TUTORIAL
FRI	LIC	SS		CS	M4	MP		CLUB ACTIVITY		PROCTORING
SAT	M4	CS		PLACEMENT TRAINING						

Subject	Subject code	Credits	Course coordinator	Subject	Subject code	Credits	Course coordinator
Engg. Mathematics IV(M4)	15MAT41	4	Dr. Jojy Joseph Idicula	Principles of Communication Systems(PCS)	15EC45	4	Prof Laxmi Sagar
Micoprocessor(MP)	15EC42	4	Prof Vidya Devi	Linear Integrated Circuits(LIC)	15EC46	4	Dr Ambika R
Control Systems(CS)	15EC43	4	Dr. A. Shobha Rani	Microprocessor lab(MP Lab)	15ECL47	2	Prof Vidya Devi
Signals and Systems(SS)	15EC44	4	Prof Saneesh Cleatus	Linear IC's and Communication lab(LIC Lab)	15ECL48	2	Dr Ambika R

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**TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18**

Wef : 1/02/2018

**SEMESTER : IV**  
**BRANCH : ECE**

**SECTION : B**  
**CLASS ROOM : BSN CR 204**  
**CLASS TEACHER : PROF SHIVARUDRAIAH B**

**Laboratory batches:**

<b>B11: 1BY16EC051 TO 1BY16EC064 (13)</b>	<b>B12: 1BY16EC065 TO 1BY16EC080 (13)</b>	<b>B21: 1BY16EC081 TO 1BY16EC094 (14)</b>	<b>B22: 1BY16EC095 TO 1BY16EC415 (14)</b>
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	8.30 - 9.20	9.20 - 10.10	10.10 - 10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 1.00	1.00 - 2.00	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
<b>MON</b>	PCS	M4	<b>TEA BREAK</b>	CS	SS	TUTORIAL	<b>LUNCH BREAK</b>	LIC	CO-CURRICULAR ACTIVITY	
<b>TUE</b>	MP	PCS		TUTORIAL	M4	SS		CS	DIP MATHS	
<b>WED</b>	LIC	MP		M4	SPORTS/CULTURAL			LIC LAB/ MP LAB		
<b>THUR</b>	M4	LIC		PCS	CS	MP		LIC LAB/ MP LAB		
<b>FRI</b>	CS	PCS		SS	LIC	M4 TUTORIAL		CLUB ACTIVITY		PROCTORING
<b>SAT</b>	SS	MP		PLACEMENT TRAINING						

<i>Subject</i>	<i>Subject code</i>	<i>Credits</i>	<i>Course coordinator</i>	<i>Subject</i>	<i>Subject code</i>	<i>Credits</i>	<i>Course coordinator</i>
Engg. Mathematics IV(M4)	15MAT41	4	Prof kallur V Vijaya Kumar	Principles of Communication Systems(PCS)	15EC45	4	Prof Rashmi N
Micoprocessor(MP)	15EC42	4	Prof Shivarudraiah	Linear Integrated Circuits(LIC)	15EC46	4	Prof Thyagaraj T
Control Systems(CS)	15EC43	4	Dr. A. Shobha Rani	Microprocessor lab(MP Lab)	15ECL47	2	Prof Shivarudraiah
Signals and Systems(SS)	15EC44	4	Prof Saneesh Cleatus	Linear IC's and Communication lab(LIC Lab)	15ECL48	2	Prof Thyagaraj T

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 TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

Wef : 1/02/2018

**SEMESTER : VI**  
**BRANCH : ECE**

**SECTION : A**  
**CLASS ROOM : BSN CR 504**  
**CLASS TEACHER : PROF MAMATHA K R**

**Laboratory batches: A1: 1BY15EC001 TO 1BY15EC031 (26)**  
**A2: 1BY15EC032 TO 1BY15EC099 (25)**

	8.30 -9.20	9.20 -10.10	10.10 - 10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 1.00	1.00 - 2.00	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
<b>MON</b>	OPEN ELECTIVE	CCN	<b>TEA BREAK</b>	<b>EMBEDDED LAB/CN LAB</b>			<b>LUNCH BREAK</b>	DC	<b>SPORTS/CULTURAL</b>	
<b>TUE</b>	VLSI	OPEN ELECTIVE		<b>EMBEDDED LAB/CN LAB</b>				PROFESSIONAL ELECTIVE	ARM	REMEDIAL CLASS
<b>WED</b>	VLSI	DC		ARM	PROFESSIONAL ELECTIVE	CCN		<b>PBL ACTIVITY</b>		
<b>THUR</b>	VLSI	CCN		OPEN ELECTIVE	DC	ARM		DC TUTORIAL	ARM(ACTIVITY)	CCN(ACTIVITY)
<b>FRI</b>	VLSI	ARM		<b>CO-CURRICULAR ACTIVITY</b>		PROFESSIONAL ELECTIVE		<b>CLUB ACTIVITY</b>		PROCTORING
<b>SAT</b>	CCN	DC		DC(ACTIVITY)	VLSI(ACTIVITY)					

<i>Subject</i>	<i>Subject code</i>	<i>Credits</i>	<i>Course coordinator</i>	<i>Subject</i>	<i>Subject code</i>	<i>Credits</i>	<i>Course coordinator</i>
Digital Communication(DC)	15EC61	4	Prof Mamatha K R	Professional Elective 2	15EC65X	4	Dr. Seema Singh/Prof Shilpa
Arm Microcontroller and Embedded Systems(ARM)	15EC62	4	Prof Anil Kumar D	Open Elective 2	15EC66X	4	Dr. Seema Singh
VLSI Design(VLSI)	15EC63	4	Dr. M C Hanumantharaju	Embedded Microcontroller Lab(ARM lab)	15ECL67	2	Prof Anil Kumar D
Computer Communication Networks(CCN)	15EC64	4	Prof Ravindra Asundi	Computer Networks Lab(CN Lab)	15ECL68	2	Prof Ravindra Asundi

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**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**  
 TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

Wef : 1/02/2018

**SEMESTER : VI**  
**BRANCH : ECE**

**SECTION : B**  
**CLASS ROOM : BSN TR 502**  
**CLASS TEACHER : PROF ASHA G H**

**Laboratory batches: B1: 1BY15EC051 TO 1BY15EC080 (26)**  
**B2: 1BY15EC081 TO 1BY15EC0101 (25)**

	8.30 -9.20	9.20 -10.10	10.10 - 10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 1.00	1.00 - 2.00	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
<b>MON</b>	<b>OPEN ELECTIVE</b>	<b>CCN</b>	<b>TEA BREAK</b>	<b>PBL ACTIVITY</b>			<b>LUNCH BREAK</b>	<b>EMBEDDED LAB/CN LAB</b>		
<b>TUE</b>	<b>VLSI</b>	<b>OPEN ELECTIVE</b>		<b>DC</b>	<b>SPORTS/CULTURAL</b>			<b>PROFESSIONAL ELECTIVE</b>	<b>ARM</b>	<b>REMEDIAL CLASS</b>
<b>WED</b>	<b>ARM</b>	<b>DC</b>		<b>VLSI</b>	<b>PROFESSIONAL ELECTIVE</b>	<b>CCN</b>		<b>EMBEDDED LAB/CN LAB</b>		
<b>THUR</b>	<b>VLSI</b>	<b>CCN</b>		<b>OPEN ELECTIVE</b>	<b>ARM</b>	<b>DC</b>		<b>DC(TUTORIAL)</b>	<b>ARM(ACTIVITY)</b>	<b>CCN(ACTIVITY)</b>
<b>FRI</b>	<b>DC</b>	<b>VLSI</b>		<b>CO-CURRICULAR ACTIVITY</b>		<b>PROFESSIONAL ELECTIVE</b>		<b>CLUB ACTIVITY</b>		<b>PROCTORING</b>
<b>SAT</b>	<b>CCN</b>	<b>ARM</b>		<b>DC(ACTIVITY)</b>	<b>VLSI(ACTIVITY)</b>					

<i>Subject</i>	<i>Subject code</i>	<i>Credits</i>	<i>Course coordinator</i>	<i>Subject</i>	<i>Subject code</i>	<i>Credits</i>	<i>Course coordinator</i>
<i>Digital Communication(DC)</i>	<i>15EC61</i>	<i>4</i>	<i>Prof Hamsavahini R</i>	<i>Professional Elective 2</i>	<i>15EC65X</i>	<i>4</i>	<i>Dr. Seema Singh/Prof Shilpa</i>
<i>Arm Microcontroller and Embedded Systems(ARM)</i>	<i>15EC62</i>	<i>4</i>	<i>Prof Suryakanth B</i>	<i>Open Elective 2</i>	<i>15EC66X</i>	<i>4</i>	<i>Dr. Seema Singh</i>
<i>VLSI Design(VLSI)</i>	<i>15EC63</i>	<i>4</i>	<i>Prof Vidyadevi M</i>	<i>Embedded Microcontroller Lab(ARM lab)</i>	<i>15ECL67</i>	<i>2</i>	<i>Prof Suryakanth B</i>
<i>Computer Communication Networks(CCN)</i>	<i>15EC64</i>	<i>4</i>	<i>Prof Asha G H</i>	<i>Computer Networks Lab(CN Lab)</i>	<i>15ECL68</i>	<i>2</i>	<i>Prof Asha G H</i>

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 TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

**SEMESTER : VIII**  
**BRANCH : ECE**

Wef : 1/02/2018  
**SECTION : A**  
**CLASS ROOM : CR 206**  
**CLASS TEACHER : PROF SHASHIKALA J**

	8.30 -9.20	9.20 -10.10	10.10 -10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 1.00	1.00 - 2.00	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
<b>MON</b>	<b>PROJECT WORK</b>									
<b>TUE</b>	<b>PROJECT WORK</b>									
<b>WED</b>	DSS	WC	<b>TEA BREAK</b>	GSM	NS	<b>LUNCH BREAK</b>				
<b>THUR</b>	WC	DSS		NS	GSM					
<b>FRI</b>	NS	GSM		DSS	WC			<b>CLUB ACTIVITY</b>		<b>PROCTORING</b>
<b>SAT</b>	GSM	NS		WC	DSS					

<i>Subject</i>	<i>Subject code</i>	<i>Course coordinator</i>		<i>Subject</i>	<i>Subject code</i>	<i>Course coordinator</i>
Wireless Communication(WC)	10EC81	Prof Rashmi N		Project Work	10ECP85	Prof Sabina/ Prof Jagannath
Digital Switching Systems(DSS)	10EC82	Prof ShilpaH		Seminar	10ECS86	Prof Asha G H
Network Security(NS)	10EC832	Prof Shashikala J				
GSM	10EC843	Prof Laxmisagar				

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**TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18**

Wef : 1/02/2018

**SEMESTER : VIII**  
**BRANCH : ECE**

**SECTION : B**

**CLASS ROOM : CR 207**  
**CLASS TEACHER : PROF JAGANNATHA K B**

	8.30 - 9.20	9.20 - 10.10	10.10 - 10.30	10.30 - 11.20	11.20 - 12.10	12.10 - 1.00	1.00 - 2.00	2.00 - 2.50	2.50 - 3.40	3.40 - 4.30
<b>MON</b>	<b>PROJECT WORK</b>									
<b>TUE</b>	<b>PROJECT WORK</b>									
<b>WED</b>	WC	DSS	<b>TEA BREAK</b>	NS	GSM		<b>LUNCH BREAK</b>			
<b>THUR</b>	GSM	DSS		NS	WC					
<b>FRI</b>	NS	GSM		WC	DSS			<b>CLUB ACTIVITY</b>		<b>PROCTORING</b>
<b>SAT</b>	DSS	GSM		WC	NS					

<i>Subject</i>	<i>Subject code</i>	<i>Course coordinator</i>		<i>Subject</i>	<i>Subject code</i>	<i>Course coordinator</i>
<i>Wireless Communication(WC)</i>	<i>10EC81</i>	<i>Prof Vinutha B</i>		<i>Project Work</i>	<i>10ECP85</i>	<i>Prof Sabina R/ Prof Jagannath K B</i>
<i>Digital Switching Systems(DSS)</i>	<i>10EC82</i>	<i>Prof Anil Kumar D</i>		<i>Seminar</i>	<i>10ECS86</i>	<i>Prof Asha G H</i>
<i>Network Security(NS)</i>	<i>10EC832</i>	<i>Prof Sabina R</i>				
<i>GSM</i>	<i>10EC843</i>	<i>Prof Jagannath K B</i>				

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TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

Wef : 1/02/2018

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YELAHANKA - BANGALORE - 64

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

w.e.f 8/08/2017

SEMESTER : III  
BRANCH : ECE

SECTION : A  
CLASS ROOM : CR 406

Day/Time	8.30 - 9.30	9.30 - 10.30	10.30 - 10.55	10.55 - 11.55	11.55 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
<b>MONDAY</b>	AE	NA	Tea Break	MAT	Lunch Break	EEM	AEC/DE lab		
<b>TUESDAY</b>	NA	9:30 to 9:55 TEA BREAK	9:55 TO 12:50 AEC/DE lab			LUNCH BREAK	MAT(T)	MAT (DIP)	MAT (DIP)
<b>WEDNESDAY</b>	EEM	EI	Tea Break	DE	Lunch Break	AE	NA	Co-curricular activities	
<b>THURSDAY</b>	AE	MAT		NA		EI	EEM	DE	Tutorial
<b>FRIDAY</b>	EI	MAT		EEM		DE	Club activities		Proctor
<b>SATURDAY</b>	MAT	DE		AE		EI			

Subject	Subject code	Credits	Course coordinator	Subject	Subject code	Credits	Course coordinator
Engg. Mathematics III (MAT)	15MAT31	4	Dr. Joju Joseph Idicula	Electronic Instrumentation (EI)	15EC35	4	Chandraprabha
Analog Electronics (AE)	15EC32	4	Dr. M C Hanumantharaju	Engineering Electromagnetics (EEM)	15EC36	4	Saneesh Cleatus T
Digital Electronics (DE)	15EC33	4	Dr. Seema Singh	Analog Electronics Laboratory (AE Lab)	15ECL37	2	Rashmi N
Network Analysis (NA)	15EC34	4	Vidyadevi	Digital Electronics and Interfacing Laboratory (DE Lab)	15ECL38	2	Vidyadevi M

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# BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT

YELAHANKA - BANGALORE - 64

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

w.e.f 8/08/2017

SEMESTER : III  
BRANCH : ECE

SECTION : B  
CLASS ROOM : BSN CR 507

Day/Time	8.30 - 9.30	9.30 - 10.30	10.30 - 10.55	10.55 - 11.55	11.55 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30	
<b>MONDAY</b>	EEM	MAT	Tea Break	NA	Lunch Break	AE	EI	DE	Tutorial	
<b>TUESDAY</b>	EI	EEM		AE		DE	MAT(T)	MAT (DIP)	MAT (DIP)	
<b>WEDNESDAY</b>	MAT	AE		AEC/DE LAB				LUNCH BREAK	Co-curricular activities	
<b>THURSDAY</b>	AE	DE		MAT	Lunch Break	NA	AEC/DE LAB			
<b>FRIDAY</b>	NA	MAT		EI		EEM	Club activities		Proctor	
<b>SATURDAY</b>	DE	NA		EEM		EI				

Subject	Subject code	Course coordinator	Subject	Subject code	Course coordinator
Engg. Mathematics III (MAT)	15MAT31	Kallur V Vijaya Kumar	Electronic Instrumentation (EI)	15EC35	Lakshmisagar
Analog Electronics (AE)	15EC32	Ambika R	Engineering Electromagnetics (EEM)	15EC36	Saneesh Cleatus T
Digital Electronics (DE)	15EC33	Hamsavahini	Analog Electronics Laboratory (AE Lab)	15ECL37	Sabina Rahaman
Network Analysis (NA)	15EC34	Vidyadevi	Digital Electronics and Interfacing Laboratory (DE Lab)	15ECL38	Hamsavahini

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TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

w.e.f 8/08/2017

SEMESTER : V  
BRANCH : ECE

SECTION : A  
CLASS ROOM : BSN CR 503

Day/Time	8.30 - 9.30	9.30 - 10.30	10.30 - 10.55	10.55 - 11.55	11.55 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30	
<b>MONDAY</b>	OPEN ELECTIVE	HDL	Tea Break	DSP	Lunch Break	ITC	DSP/HDL lab			
<b>TUESDAY</b>	Professional elective	OPEN ELECTIVE		DSP/HDL lab				LUNCH BREAK	M&E	Tutorial
<b>WEDNESDAY</b>	M&E	Professional elective		ITC	Lunch Break	DSP	Tutorial	Co-curricular activity		
<b>THURSDAY</b>	ITC	HDL		OPEN ELECTIVE		Professional elective	DSP	PBL		
<b>FRIDAY</b>	DSP	HDL		M&E		EXTRA CURRICULAR	Club activities			Proctor
<b>SATURDAY</b>	HDL	ITC		M&E						

Subject	Subject code	Course coordinator	Subject	Subject code	Course coordinator
Management & Entrepreneurship	10AL51	Ravindra A	Professional elective	10EC***	Anil Kumar/Shilpa Hiremath
Digital Signal Processing	10EC52	Mamatha K R	Open elective	10EC***	Thyagaraj/Deepa Reddy
HDL	10EC53	Suryakanth	DSP Lab	10ECL57	Mamatha K R
Information Theory and Coding	10EC54	Shashikala	HDL Lab	10ECL58	Suryakanth

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TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

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SEMESTER : V  
BRANCH : ECE

SECTION : B  
CLASS ROOM : BSN CR 504

Day/Time	8.30 - 9.30	9.30 - 10.30	10.30 - 10.55	10.55 - 11.55	11.55 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30	
<b>MONDAY</b>	OPEN ELECTIVE	DSP	Tea Break	ITC	Lunch Break	M&E	HDL	Tutorial	Tutorial	
<b>TUESDAY</b>	Professional elective	OPEN ELECTIVE		DSP		ITC	DSP/HDL lab			
<b>WEDNESDAY</b>	DSP	Professional elective		DSP/HDL lab				LUNCH BREAK	Co-curricular activity	
<b>THURSDAY</b>	ITC	M&E		OPEN ELECTIVE	Lunch Break	Professional elective	HDL	PBL		
<b>FRIDAY</b>	M&E	DSP		HDL		EXTRA CURRICULAR	Club activities			Proctor
<b>SATURDAY</b>	HDL	M&E		ITC						

Subject	Subject code	Course coordinator	Subject	Subject code	Course coordinator
Management & Entrepreneurship	10AL51	Anil Kumar D	Professional elective	10EC***	Anil kumar/Shilpa Hiremath
Digital Signal Processing	10EC52	Dr. A Shobha Rani	Open elective	10EC***	Thyagaraj/ DeepaReddy
HDL	10EC53	Jaganath	DSP Lab	10ECL57	Deepa Reddy
Information Theory and Coding	10EC54	Asha G H	HDL Lab	10ECL58	Jaganath

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TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

w.e.f 8/08/2017

SEMESTER : VII  
BRANCH : ECE

SECTION : A  
CLASS ROOM : CR 206

Day/Time	8.30 - 9.30	9.30 - 10.30	10.30 - 10.55	10.55 - 11.55	11.55 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
<b>MONDAY</b>	PE	CCN	TEA BREAK	DSPA/ANN	LUNCH BREAK	OFC	Project	Co-curricular activities	
<b>TUESDAY</b>	CCN	ESD		PE		DSPA/ANN	OFC	IP/RTS	EXTRA CURRICULAR
<b>WEDNESDAY</b>	DSPA/ANN	CCN		IP/RTS	VLSI/PE lab	ESD	VLSI/PE lab		
<b>THURSDAY</b>	IP/RTS	OFC		VLSI/PE lab			LUNCH BREAK	PE	Project
<b>FRIDAY</b>	OFC	ESD		PE	LUNCH BREAK	CCN	Club activity		Proctor
<b>SATURDAY</b>	ESD	IP/RTS		DSPA/ANN					

Subject	Subject code	Course coordinator	Subject	Subject code	Course coordinator
Computer Communication Networks	10EC71	Asha G H	DSPA / Artificial Neural Networks	10EC751/10EC753	Deepa Reddy / Rashmi N
Optical Fibre Communication	10EC72	Shivarudriah B	Real-time Systems / Image Processing	10EC762/ 10EC763	Laxmisagar / Mamatha K R
Power Electronics	10EC73	Shashikala K J	VLSI Lab	10ECL77	Lakshmisagar
Embedded System Design	10EC74	Ambika R	Power Electronics Lab	10ECL78	Shashikala J

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

w.e.f 8/08/2017

SEMESTER : VII  
BRANCH : ECE

SECTION : B  
CLASS ROOM : CR 207

Day/Time	8.30 - 9.30	9.30 - 10.30	10.30 - 10.55	10.55 - 11.55	11.55 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30
<b>MONDAY</b>	OFC	ESD	TEA BREAK	DSPA/ANN	LUNCH BREAK	CCN	PE	Co-curricular activities	
<b>TUESDAY</b>	CCN	OFC		ESD		DSPA/ANN	PE	IP/RTS	EXTRA CURRICULAR
<b>WEDNESDAY</b>	DSPA/ANN	PE		IP/RTS		CCN	OFC	Project	Project
<b>THURSDAY</b>	IP/RTS	CCN		ESD		OFC	VLSI/PE LAB		
<b>FRIDAY</b>	ESD	9:30 to 9:55 TEA BREAK	9:55 to 12:55 VLSI/PE LAB			LUNCH BREAK	Club activity		Proctor
<b>SATURDAY</b>	PE	IP/RTS	TEA BREAK	DSPA/ANN					

Subject	Subject code	Course coordinator	Subject	Subject code	Course coordinator
Computer Communication Networks	10EC71	DR. A Shobha Rani	DSPA / Artificial Neural Networks	10EC751/ 10EC753	Deepa Reddy / Rashmi N
Optical Fibre Communication	10EC72	Shivarudriah B	Real-time Systems / Image Processing	10EC762/ 10EC763	Laxmisagar / Shilpa Hiremath
Power Electronics	10EC73	Thyagaraj	VLSI Lab	10ECL77	Shivarudriah B
Embedded System Design	10EC74	Hamsavahini R	Power Electronics Lab	10ECL78	Thyagaraj

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