

BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT

(Autonomous Institute affiliated to VTU)

Scheme of Teaching and Examination: Effective from AY 2021–22 Choice Based Credit System (CBCS)

UG PROGRAM: ELECTRICAL & ELECTRONICS ENGINEERING (EEE) Semester: III Examination Cre Course **S1**. Course Teaching **Teaching Hours** Code **Course** Title dits No Dept. /Week Categor v Duration CIE SEE Total Marks Marks in Hours Marks L Т Ρ PW Fourier series, Numerical methods, statistics BS 21MTA31 1 3 3 MAT 1 1 0 4 50 50 100 and Probability Samskrutika Kannada 21KSK32 HS 2 21KBK32 Balake Kannada HS 1 0 0 0 1 1 50 50 100 UHV 21UHV33 Universal Human Values-I 3 HS 1 0 0 0 1 1 50 50 100 INT 21INT34 Internship-I 4 EEE 0 0 0 4 2 3 100 100 --5 PC 2 21EE35 Electrical Circuit Analysis EEE 2 0 0 3 3 50 50 100 6 PC 21EE36 EEE 2 2 0 0 3 3 Electrical Machines-I 50 50 100 7 PC 21EE37 EEE 2 2 0 0 3 3 50 50 Analog Electronic Circuits 100 8 21EEL38A PC EEE 0 2 0 3 1 1 50 50 100 Electrical Machines Laboratory - I 21EEL38B 9 PC EEE 0 2 0 3 1 1 50 100 50 Analog Electronics Laboratory 21EEL38C 10 PC 0 1 2 0 3 EEE 1 50 50 100 Electrical Simulation Laboratory TOTAL 10 7 4 20 550 450 1000 11

Course prescribed to Lateral entry Diploma holders admitted to III Semester B.E.													
1	NCMC	21DIP31A	Diploma Mathematics-I	MAT	3	0	0	0		3	100		100

- The Lateral entry students have to undergo Internship-I during the intervening vacation of III and IV semesters.
- The Assessment Pattern for 1/2/3 credit courses shall be done as per the VTU guidelines.
- BS-MTX (X-Variable) Eg: Core branches: ME, CV, EEE, ETE, ECE-MTA, Digital branches: CSE, ISE, AIML- MTB.
- Additional Mathematics I and II shall not be considered for vertical progression as well as for the calculation of SGPA and CGPA, but completion of the courses shall be mandatory for the award of degree.
- Successful completion of the course Additional Mathematics-II shall be indicated as satisfactory in the grade card. Non completion of the same shall be indicated as unsatisfactory.

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING

Choice Based Credit System (CBCS)

SEMESTER – III

Fourier Series, Numerical Methods, Statistics and Probability (3:1:1) 4

(Common to ECE, ETE, EEE, MECH & CIVIL Branches)

(Effective from the academic year 2021-22)

Course Code	21MTA31	CIE Marks	50
Teaching Hours/Week (L:T:P)	3:1:1	SEE Marks	50
Total Number of Contact Hours	50	Exam Hours	3

Course Objectives:

This course will enable students to:

- 1. Apply the concepts of Fourier series, Fourier transforms, Difference equations and Z-transforms in the field of engineering.
- 2. Apply the concept of Numerical Techniques, and probability distribution to analyze problems arising in Science and Engineering field.
- 3. Apply the knowledge of interpolation/extrapolation and Numerical Integration technique whenever analytical methods fail or very complicated to offer solutions.

Module – I

Introduction: Understanding of Transform Calculus, Numerical methods & their applications in Engineering, Economics and Statistics.

Statistical Methods: Correlation-Karl Pearson's coefficient of correlation – problems, Regression lines, lines of regression (without proof) –Problems.

Curve fitting: Curve fitting by the method of least squares- fitting of the curves of the form, y = ax + b, $y = ax^2 + bx + c$ and $y = ae^{bx}$.

Calculus of variation: Variation of a function and a functional, Extremal of a functional, Euler's equation, Standard variational problems.

Self-Learning Component: Fitting of the curves $y = a x^b$ and $y = a b^x$.

Lab Session 1:

- 1. Determination of polynomial using method of Least Square Curve Fitting.
- 2. Relation between variables: correlation, Regression. (10 Hours)

Module – II

Finite Differences: Forward and backward differences, Newton's forward and backward interpolation formulae, Divided differences- Newton's divided difference formula, Lagrange's interpolation formula and inverse interpolation formula (all formulae without proof) - problems.

Numerical Integration: Simpson's $(1/3)^{rd}$ and $(3/8)^{th}$ rules, Weddle's rule (without proof) – problems.

Self-Learning Component: Trapezoidal rule.

Lab Session 2:

1. Numerical solution using Newton's Forward / Backward interpolation formula.

2. Numerical integration using Simpson's One-third rule.(10 Hours)

Module – III

Fourier Series: Dirichlet's conditions, Fourier Series of periodic functions of period 2π and arbitrary period. Half range Fourier Series, Practical harmonic analysis over the period 2π . **Self-Learning Component:** Complex Fourier Series.

Lab Session 3:

- 1. Obtain the Fourier series of a function.
- 2. Finding Fourier series by practical Harmonic Analysis. (10 Hours)

Module – IV

Fourier Transforms: Infinite Fourier transforms, Fourier Sine and Cosine transforms. Inverse Fourier transforms - problems.

Z-transforms: Difference equations, basic definition, Z-transform-definition, Standard Ztransforms (only formula), Damping rule, Shifting rule (without proof) and problems, Inverse Z-transforms – problems, Solution of Difference equations using Z transforms.

Self-Learning Component: Proofs of Z-transformation of standard functions.

Lab Session 4:

- 1. Obtain the Fourier Transform of a function.
- 2. Obtain the solution of difference equation using Z Transforms. (10 Hours)

Module – V

Probability Distributions: Random variables (discrete and continuous), probability mass/density functions. Binomial distribution, Poisson distribution. Exponential and Normal distributions, problems,

Self-Learning Component: Uniform distribution.

Lab Session 5:

- 1. Compute Pdf/Pmf for given data.
- 2. Compute and plot the probability density function for Normal Distribution, Binomial Distribution, Exponential Distribution, Poisson Distribution.

(10 Hours)

Recap/Summary of the Course.

Course Outcomes:

The students will be able to:

- CO1: Make use of the concepts of method of least squares, correlation and regression analysis to fit a suitable mathematical model for the statistical data and calculus of variation to evaluate extremal of a functional.
- CO2: Apply the knowledge of Numerical Methods in the modeling of various physical and engineering phenomena.
- CO3: Apply Fourier series to study the behavior of periodic functions and Fourier transforms and Z-transforms to illustrate discrete/continuous function arising in wave and heat propagation, signals and systems.
- CO4: Develop probability distribution of discrete, continuous random variables and joint probability distribution occurring in digital signal processing, information theory and design engineering.

Question paper pattern:

SEE will be conducted for 100 marks.

- The question paper will have ten questions. Each question is set for 20 marks.
- There will be 2 questions from each module. Each of the two questions under a module (with a maximum of 3 sub-questions), should have a mix of topics under that module.
- The students have to answer 5 full questions, selecting one full question from each module.

CIE will be conducted for 100 marks.

• Three Unit Tests each of **20 Marks** (Duration 01 hour).

- Two assignments each of 10 Marks.
- Two alternate assessment tools (AATs) from the list shall be planned to attain the COs and POs for **20 Marks** (duration 01 hours).
- The sum of three tests, two assignments, and AATs will be out of 100 marks and will be scaled down to 50 marks.

Textbooks:

- 1. B.S. Grewal, "Higher Engineering Mathematics", 43rd Edition, Khanna Publishers, 2015.
- 2. E. Kreyszig, "Advanced Engineering Mathematics", 10th Edition, John Wiley & Sons, 2015.
- 3. B.V. Ramana, "Higher Engineering Mathematics", 6th Edition, Tata McGraw-Hill, 2010. **References:**
- 1. N.P. Bali, Manish Goyal, "A Text Book of Engineering Mathematics", Laxmi Publishers, 2014.
- 2. H.K. Dass, Er. Rajnish Verma, "Higher Engineering Mathematics", 3rd Edition, S. Chand publishers, 2014.
- 3. P. Kandasamy, K. Thilagavathi, K. Gunavathi, "Engineering Mathematics", Vol. III, 2001.
- 4. S.S. Sastry, "Introductory Methods of Numerical Analysis", 4th Edition, Prentice Hall of India, 2010.

B.E. ELECTRICAL A Choice E	ND ELECTRONI Based Credit System (SEMESTER – III				
ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡ	Samskrutika Kan	nada (1:0:0):1			
	the academic year				
ವಿಷಯ ಸಂಕೇತ Course Code	21KSK32/42	ನಿರಂತರ ಆಂತರಿಕ ಮೌಲ್ಯಮಾಪನದ	50		
		ಅಂಕಗಳು CIE Marks			
ಒಂದು ವಾರಕ್ಕೆ ಬೋಧನಾ ಅವಧಿ Teaching	1-0-0	ಸಮಿಸ್ಟರ್ ಅಂತ್ಯದ ಪರೀಕ್ಷೆಯ ಅಂಕಗಳ			
hours/Week (L: T:P)		SEE Marks	50		
ಒಟ್ಟು ಬೋಧನಾ ಅವಧಿ Total Number of	13	ಪರೀಕ್ಷೆಯ ಅವಧಿ Exam Hours	01		
contact hours		∽ ,			
ಸಂಸ್ಕೃತಿಕ ಕನ್ನಡ ಪಠ್ಯದ ಕಲಿಕೆಯ ಉದ್ದೇಶಗಳ 1. ವೃತ್ತಿಪರ ಪದವಿ ವಿದ್ಯಾರ್ಥಿಗಳಾಗಿರುವುದರಿಂ ಮಾಡಿಕೊಡುವುದು. 2. ಕನ್ನಡ ಸಾಹಿತ್ಯದ ಪ್ರಧಾನ ಭಾಗವಾದ ಆ ಪರಿಚಯಿಸಿ ವಿದ್ಯಾರ್ಥಿಗಳಲ್ಲಿ ಸಾಹಿತ್ಯ ಮತ್ತು ಸಂ 3. ತಾಂತ್ರಿಕ ವ್ಯಕ್ತಿಗಳ ಪರಿಚಯವನ್ನು ಹಾಗೂ ಆ	ದ ಕನ್ನಡ ಭಾಷೆ, ಸಾ ೨ಧುನಿಕ ಪೂರ್ವ ವ ೧ಸ್ಕೃತಿಯ ಬಗ್ಗೆ ಅರಿವು	ುತ್ತು ಆಧುನಿಕ ಕಾವ್ಯಗಳನ್ನು ಸಾಂ ಹಾಗೂ ಆಸಕ್ತಿಯನ್ನು ಮೂಢಿಸುವ	ಕೇತಿವಾಗಿ		
	ಘಟಕ–1				
ಲೇಖನಗಳು:					
ಕರ್ನಾಟಕ ಏಕೀಕರಣ: ಒಂದು ಅಪೂರ್ವ ಚರಿ					
ಆಡಳಿತ ಭಾಷೆಯಾಗಿ ಕನ್ನಡ–ಡಾ.ಎಲ್.ತಿಮ್ಮೇಶ		ಸಮೂರ್ತಿ 2 ಗಂ	ಟೆಗಳು		
	ಘಟಕ–2				
ಆಧುನಿಕ ಪೂರ್ವದ ಕಾವ್ಯ ಭಾಗ: ವಚನಗಳು–ಬಸವಣ್ಣ, ಅಕ್ಕಮಹಾದೇವಿ, ಆಯ್ದಕ್ಕಿ ಕೀರ್ತನೆಗಳು–ಅದರಿಂದೇನು ಫಲ ಇದರಿಂದೇನ ತಲ್ಲಣಿಸದಿರು ಕಂಡ್ಯ ತಾಳು ಮನವೇ–ಕನಕದಾಸ	, ಫಲ–ಮರಂದರದಾ	ಸರು	ಂಟೆಗಳು		
	ಘಟಕ–3				
ಆಧುನಿಕ ಕಾವ್ಯ ಭಾಗ: ಡಿ.ವಿ.ಜಿ.ಯವರ ಮಂಕು ತಿಮ್ಮನ ಕಗ್ಗದಿಂದ ಆಯ್ದ ಕೆಲವು ಭಾಗಗಳು. ಹೊಸ ಬಾಳಿನ ಗೀತೆ: ಕುವೆಂಪು 2 ಗಂಟೆಗಳು 2 ಗಂಟೆಗಳು					
	ಘಟಕ–4				
ತಾಂತ್ರಿಕ ವ್ಯಕ್ತಿಗಳ ಪರಿಚಯ, ವಿಜ್ಞಾನ ಮತ್ತು ತಂತ	0.				
ಡಾ.ಸರ್.ಎಂ.ವಿಶ್ವೆಶ್ವರಯ್ಯ:ವ್ಯಕ್ತಿ ಮತ್ತು ಐತಿಹ್ಯ–ಎ ಕರಕುಶಲ ಕಲೆಗಳು ಮತ್ತು ಪರಂಪರೆಯ ವಿಜ್ಞಾ		ವಳ್ಳಿ 4 ಗಂ	ಟೆಗಳು		
	ಘಟಕ–5				
ಪ್ರವಾಸ ಕಥನ: ಮೆಗಾನೆ ಎಂಬ ಗಿರಿಜನ ಪರ್ವತ: ಹಿ.ಚಿ.ಬೋರ	ರಲಿಂಗಯ್ಯ	2 ಗಂಟ	ತೆಗಳು		

ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡ ಪಠ್ಯ ಕಲಿಕೆಯಿಂದ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ಆಗುವ ಅನುಕೂಲಗಳು ಮತ್ತು ಫಲಿತಾಂಶಗಳು: Course outcomes

- 1. ಕನ್ನಡ ಭಾಷೆ, ಸಾಹಿತ್ಯ ಮತ್ತು ಕನ್ನಡದ ಸಂಸ್ಕೃತಿಯ ಪರಿಚಯವಾಗುತ್ತದೆ.
- ಕನ್ನಡ ಸಾಹಿತ್ಯದ ಆಧುನಿಕ ಪೂರ್ವ ಮತ್ತು ಆಧುನಿಕ ಕಾವ್ಯಗಳು ಮತ್ತು ಸಂಸ್ಕೃತಿಯ ಬಗ್ಗೆ ಆಸಕ್ತಿಯು ಮೂಡುತದೆ.
- 3. ತಾಂತ್ರಿಕ ವ್ಯಕ್ತಿಗಳ ಪರಿಚಯವಾಗುತ್ತದೆ.
- 4. ಕನ್ನಡ ಭಾಷಾಭ್ಯಾಸ, ಸಾಮಾನ್ಯ ಕನ್ನಡ ಹಾಗೂ ಆಡಳಿತ ಕನ್ನಡದ ಪದಗಳ ಪರಿಚಯವಾಗುತ್ತದೆ

Assessment Details (both CIE and SEE): Kannada, Constitution of India and Professional Ethics and Universal Human Values

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum mark which is 20 marks. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 35% (18 Marks out of 50) in the semester-end examination (SEE), and a minimum of 40% (40 marks out of 100) in the sum total of the CIE and SEE taken together

Continuous Internal Evaluation:

Three Tests each of 20 Marks. (duration 01 hour)

- 1. First test at the end of 5th week of the semester
- 2. Second test at the end of the 10th week of the semester
- 3. Third test at the end of the 15th week of the semester
- Two assignments each of **10 Marks**
- 4. First assignment at the end of 4th week of the semester
- 5. Second assignment at the end of 9th week of the semester

Two alternate assessment tools (AATs) from the list shall be planned to attain the COs and POs for **20** Marks

6. At the end of the 13th week of the semester

The sum of three tests, two assignments and AATs will be out of 100 marks and will be scaled down to 50 marks.

CIE methods/question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination (SEE):

SEE will be conducted as per the scheduled timetable, with common question papers for the subject.

- The question paper will have 50 questions. Each question is set for 01 mark.
- SEE Pattern will be in MCQ Model (Multiple Choice Questions) for 50 marks. The duration of the examination is 01 Hour.

Textbook:

ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡ

ಡಾ.ಹಿ.ಚೆ ಬೋರಲಿಂಗಯ್ಯ ಮತ್ತು ಡಾ. ಎಲ್. ತಿಮ್ಮೇಶ,

ಪ್ರಸಾರಾಂಗ ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING Choice Based Credit System (CBCS) SEMESTER – III						
ಬಳಕೆ	ಕನ್ನಡ Balake Kann	ada (1:0:0):1				
	(Common to all Br					
-	tive from the academ	nc year 2021-22) ನಿರಂತರ ಆಂತರಿಕ ಮೌಲ್ಯಮಾಪನದ	-			
ವಿಷಯ ಸಂಕೇತ Course Code	21KBK32/42	ಅಂಕಗಳು CIE Marks	50			
ಒಂದು ವಾರಕ್ಕೆ ಬೋಧನಾ ಅವಧಿ	1-0-0	. ಸೆಮಿಸ್ಟರ್ ಅಂತ್ಯದ ಪರೀಕ್ಷೆಯ ಅಂಕಗಳು				
Teaching hours/Week (L: T:P)		SEE Marks	50			
ಒಟ್ಟು ಬೋಧನಾ ಅವಧಿ Total	13	ಪರೀಕ್ಷೆಯ ಅವಧಿ Exam Hours	01			
Number of contact hours						
ಬಳಕೆ ಕನ್ನಡ ಪಠ್ಯ ಕಲಿಕೆಯ ಉದ್ದೇಶಗಳು Co	urse Learning Obje	ctives:				
and healthy life.	ten and understand Kannada language a		nortable			
	Module-1					
Easy learning of a Kannada Lar Listening and Speaking Activities Key to Transcription. ವೈಯಕ್ತಿಕ, ಸ್ವಾಮ್ಯಸೂಚಕ/ಸಂಬಂಧಿತ ಸರ್ವ Personal Pronouns, Possessive F	5 ನಾಮಗಳು ಮತ್ತು ಪ್ರಶ್ನಾರ್ಥ		servation, 3 Hours			
	Module-2		0 110 110			
ನಾಮಪದಗಳ ಸಂಬಂಧಾರ್ಥಕ ರೂಪಗಳು,		ುತ್ತು ಸಂಬಂಧವಾಚಕ ನಾಮಪದಗಳು				
Possessive forms of nouns, dubit	tive question and Re	lative nouns				
ಗುಣ, ಪರಿಮಾಣ ಮತ್ತು ವರ್ಣಬಣ್ಣ ವಿ	ಶೇಷಣಗಳು, ಸಂಖ್ಯಾವಾಚ	ಕಗಳು Qualitative, Quantitative a	ind Color			
Adjectives, Numerals	ಲು – ಸಪನಾ ವಿಶಕಿ ಪಾ	ತ್ಯಯ (ಅ, ಅದು, ಅವು, ಅಲ್ಲಿ) Predictiv	ve Forms			
Locative Case		3 Hours				
	Module-3	5 Hour				
	ಂಖಾವಾಚಕಗಳು Dative	cases and Numerals				
ಸಂಖ್ಯಾಗುಣವಾಚಕಗಳು ಮತ್ತು ಬಹುವಚನ	5					
		Defective /Negative Verbs an	d Colour			
Adjectives	<i>ه</i> ـ	3 Hours				
	Module-4					
ಅಪ್ಪಣೆ/ಒಪ್ಪಿಗೆ, ನಿರ್ದೇಶನ, ಪ್ರೋತ್ಸಾಹ ಮತ	್ತು ಒತ್ತಾಯ ಅರ್ಥರೂಪ ಪ	ದಗಳು ಮತ್ತು ವಾಕ್ಯಗಳು Permission,				
Commands, encouraging and ur		= 0				
		ವನೀಯ ಪ್ರಕಾರಗಳು Accusative Cases	and			
Potential Forms used in General	. .		lours			
	Module-5					

"ಇರು ಮತ್ತು ಇರಲ್ಲ" ಸಹಾಯಕ ಕ್ರಿಯಾಪದಗಳು, ಸಂಭಾವ್ಯಸೂಚಕ ಮತ್ತು ನಿಷೇಧಾರ್ಥಕ ಕ್ರಿಯಾ ಪದಗಳು Helping verbs "iru and iralla" Corresponding Future and Negation Verbs

ಹೋಲಿಕೆ(ತರತಮ), ಸಂಬಂಧಸೂಚಕ ಮತ್ತು ವಸ್ತು ಸೂಚಕ ಪ್ರತ್ಯಯಗಳು ಮತ್ತು ನಿಷೇದಾರ್ಥಕ ಪದಗಳ ಬಳಕೆ

Comparitive, Relationship, Identification and Negation words

2 Hours

ಬಳಕೆ ಕನ್ನಡ ಪಠ್ಯ ಕಲಿಕೆಯಿಂದ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ಆಗುವ ಅನುಕೂಲಗಳು ಮತ್ತು ಫಲಿತಾಂಶಗಳು: Course outcomes

At the end of the Course, The Students will be able

- **1.** To understand the necessity of learning of local language for comfortable life.
- 2. To Listen and understand the Kannada language properly.
- **3.** To speak, read and write Kannada language as per requirement.
- **4.** To communicate (converse) in Kannada language in their daily life with kannada speakers.
- **5.** To speak in polite conversation.

Assessment Details (both CIE and SEE): Kannada, Constitution of India and Professional Ethics and Universal Human Values

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum mark which is 20 marks. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 35% (18 Marks out of 50) in the semester-end examination (SEE), and a minimum of 40% (40 marks out of 100) in the sum total of the CIE and SEE taken together

Continuous Internal Evaluation:

Three Tests each of **20 Marks.** (duration 01 hour)

- 1. First test at the end of 5th week of the semester
- 2. Second test at the end of the 10th week of the semester
- 3. Third test at the end of the 15th week of the semester
- Two assignments each of 10 Marks
- 4. First assignment at the end of 4th week of the semester
- 5. Second assignment at the end of 9th week of the semester

Two alternate assessment tools (AATs) from the list shall be planned to attain the COs and POs for **20** Marks

6. At the end of the 13th week of the semester

The sum of three tests, two assignments and AATs will be out of 100 marks and will be scaled down to 50 marks.

CIE methods/question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination (SEE):

SEE will be conducted as per the scheduled timetable, with common question papers for the subject. The question paper will have 50 questions. Each question is set for 01 mark.

SEE Pattern will be in MCQ Model (Multiple Choice Questions) for 50 marks. The duration of the examination is 01 Hour.

Textbook:

ಬಳಕೆ ಕನ್ನಡ ಲೇಖಕರು: ಡಾ. ಎಲ್. ತಿಮ್ಮೇಶ ಪ್ರಸಾರಾಂಗ, ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

_	CAL AND ELECTRONICS ENGI Choice Based Credit System (CBCS) SEMESTER – III	NEERING
Univer	sal Human Values- I (1:0:0) 1	
	ive from the academic year 2021-2022	2)
Course Code	21UHV33	CIE Marks 50
Teaching Hours/Week (L: T:P)	1-0-0	SEE Marks 50
Total Number of Lecture Hours	13	Exam Hours 01
Course objectives:	10	
This introductory course is intende	ed to	
	ased on self-exploration about themsel	lves (human being).
2. Understand harmony in the hum		(naman semig).
3. Strengthening of self-reflection.		
4. Develop commitment and coura	ge to act.	
F	Module – 1	
Preamble: Significance and Scop	e of the course, Importance of the co	urse in societal, political an
economic growth of the nation.	r in the second s	, I
e	n: Understanding Value Education; N	Need and Basic guidelines for
Value Education; Scope and Proce	-	C
· •	for Value Education: What is self-	exploration; Process of sel
exploration.		
Case study and Group Discussion	n	2 Hour
	Module – 2	
and Physical Facilities. Case study and Group Discussio	asic Human Aspirations; Need for righ	it understanding; Relationshi
Hours	Module – 3	
Understanding human being	as co-existence of the self and the	a Dady Understanding on
8 8	s of the Self and the Body- Quantit	e e
Assuming, Recognizing and fulfill	• •	ative, Quantative, Knowing
Case study and Group Discussio		
Hours		
	Module – 4	
	self; Activities in self; Power of exp as a result of pre-conditioned desire; R	
Case study and Group Discussion	1 · · · ·	3 Hour
Case study and Group Discussion	Module – 5	5 1100
Hormony with Dodger Hormon		athron IIndonaton din 1
	of self with the body-Sanyama and Sva the body; Protection of the body; Right eeds.	
Case study and Group Discussion	n	3 Hour
Course outcomes: The students w	ill be able to:	
	education, self-exploration and harmo and skills, Self and the Body, Intention a	-

Assessment Details (both CIE and SEE): Kannada, Constitution of India and Professional Ethics and Universal Human Values

The weightage of Continuous Internal Evaluation (CIE) is 50% and for Semester End Exam (SEE) is 50%. The minimum passing mark for the CIE is 40% of the maximum mark which is 20 marks. A student shall be deemed to have satisfied the academic requirements and earned the credits allotted to each subject/ course if the student secures not less than 35% (18 Marks out of 50) in the semester-end examination (SEE), and a minimum of 40% (40 marks out of 100) in the sum total of the CIE and SEE taken together

Continuous Internal Evaluation:

Three Tests each of **20 Marks.** (duration 01 hour)

- 1. First test at the end of 5th week of the semester
- 2. Second test at the end of the 10th week of the semester
- 3. Third test at the end of the 15th week of the semester
- Two assignments each of **10 Marks**
- 4. First assignment at the end of 4th week of the semester
- 5. Second assignment at the end of 9th week of the semester

Two alternate assessment tools (AATs) from the list shall be planned to attain the COs and POs for **20** Marks

- 6. At the end of the 13th week of the semester
- The sum of three tests, two assignments and AATs will be out of 100 marks and will be scaled down to 50 marks.

CIE methods/question paper is designed to attain the different levels of Bloom's taxonomy as per the outcome defined for the course.

Semester End Examination (SEE):

SEE will be conducted as per the scheduled timetable, with common question papers for the subject. The question paper will have 50 questions. Each question is set for 01 mark.

SEE Pattern will be in MCQ Model (Multiple Choice Questions) for 50 marks. The duration of the examination is 01 Hour.

Textbooks

1. The Textbook *A Foundation Course in Human Values and Professional Ethics*, R R Gaur, R Asthana, G P Bagaria, 2nd Revised Edition, Excel Books, New Delhi, 2019. ISBN978-93-87034-47-1

2. The Teacher's Manual for *A Foundation Course in Human Values and Professional Ethics*, R R Gaur, R Asthana, G P Bagaria, 2nd Revised Edition, Excel Books, NewDelhi, 2019. ISBN 978-93-87034-53-2

References

- 1. Jeevan Vidya: Ek Parichaya, A Nagaraj, Jeevan Vidya Prakashan, Amar kantak, 1999.
- 2. Human Values, A.N. Tripathi, New Age Intl. Publishers, New Delhi, 2004.
- 3. Slow is Beautiful Cecile Andrews
- 4. Vivekananda Romain Rolland (English)

Relevant websites, documentaries

- 1. Value Education websites, <u>http://uhv.ac.in</u>,
- 2. Story of Stuff, *http://www.storyofstuff.com*

D.E. ELECI	FRICAL AND EL		
	Choice Based Cre	dit System (CB(STER – III	CS)
	Inter/Intra-Insti		chin
ſF	affective from the a		
Credits: 2	Course Code:	21INT34	CIE:100 Marks
Duration: 3 Weeks	1		SEE: NA
Schedule:			
• During the intervenin	g vacation of II and	III semester for	r students admitted to I semester.
• During the intervenin	g vacation of III and	d IV semester fo	or lateral entry diploma
studentsadmitted to I	II semester.		
Course Outcomes: students	will be able to		
CO1. Acquire academic/	career/ personal o	verall skill/ kno	wledge development.
CO2. Perceive ample opp tosociety and envi	*	essional growth	and achievement with relevance
CO3. Expose to real job w	orld environment	and gain praction	cal knowledge with experience.
· ,	alities, teamwork, o	0 1	cooperation, and facility in
0			
CO5. Intensify creativity, expression.	artistry, curiosity,	imagination, ini	novation and personal

Rubrics for Internal Evaluation (Total Marks: 100)

Indicator	Poor	Average	Good	Excellent
Acquired	Not gained any skill /	Partial skill/Knowledge	Average	Complete
skills or	knowledge.	gained.	skill/knowledge	skill/knowledge
knowledge	OR		gained.	gained.
(10 Marks)	Attended a few	Only Block	Lack of Technical/	All Skills Acquired.
	sessions.	Diagram/Notes/	Knowledge.	8-10 Marks
(CO1)	0-1 Marks	Description.	5-7 Marks	
		2-4 Marks		
Weekly	Weekly report not	One Weekly report	Two weekly reports	All three weekly
report	submitted.	submitted.	submitted.	reports submitted.
(10 Marks)	OR			
	Few days report was			
	submitted.	2-4 Marks	5-7 Marks	8-10 Marks
(CO6)	0-1 Marks			

Presentati on (10 Marks) (C05)	Absence for presentation. OR Presented after the due date. 0-1 marks	Information is lacking/ unclear and communicated in such a way that the audience cannot understand the purpose of the evidence of work and internship experiences. 2-4 Marks	Information is not presented in a clear manner and many details are missing related to the evidence work and internship experiences. 5-7 Marks	Information is presented in such a way that the audience can understand the purpose of the evidence of work and internship experiences. 8-10 Marks
Practical Knowledge (10 Marks) (CO3)	Not gained any practical knowledge. OR Able to define basic concepts. 0-1 Marks	Partial practical Knowledge gained. Less hands-on experience. 2-4 Marks	Average practical knowledge gained. Only few models are exhibited. 5-7 Marks	Complete practical knowledge gained. 8-10 Marks
Societal and environme nt al relevance (10 Marks) (CO2)	No relevance to society or environment (At- least one relevance). 0-1 Marks	Partial relevance to society or environment. 2-4 Marks	Average relevance to society or environment. 5-7 Marks	Directly Relevant to society or environment. 8-10 Marks
Viva (10 Marks) (CO4)	Does not know any information. OR Fair leadership quality/ teamwork/ cooperation. 0-1 Marks	Provides irrelevant information for all questions. Good leadership quality/ teamwork/ cooperation. 2-4 Marks	Provides incomplete information for all questions. Better leadership quality/ teamwork/ cooperation. 5-7 Marks	Provides complete information for all questions. Outstanding leadership quality/ teamwork/ cooperation. 8-10 Marks
Report (40 Marks)	Does not submit the report.	Report submitted does not fulfill the prescribed format	Report submitted partially fulfills the prescribed format	Report submitted fulfills the prescribed format

	ND ELECTRONI ased Credit System (IESTER – III		
Electrical	Circuit Analysis (m the academic year		
Course Code	21EE35		50
Teaching Hours/Week (L:T:P)	2:2:0	SEE Marks	50
Total Number of Contact Hours	40	Exam Hours	3
 This course will enable students to : Understand basic laws, source traanalyzing electrical circuits. Use of network theorems. Appreciate the importance of init R-L and R-C circuits. Analyze resonant circuits, two por inputs. Analyze electric networks using I Introduction: Scope of the course in Ind When Electrical Science became t Direct and Indirect Market share Scope of Design and Analysis of S Percentage of Finance in Design a Basic Concepts: Active and Passive Elements, Concepts o source shifting, Network reduction Methand independent sources for DC and AC 	ial conditions, their rt networks and no <u>Laplace transform</u> <u>Module – 1</u> ustry and Econom the Electrical Indus of Electrical Indus ystems and Analysis of Ideal and Praction nods, Mesh and No	r evaluation and transie etworks with non-sinusc s y: stry? stry cal sources, source trans ode analysis with linear o	nt analysis of bidal
and macpenaent sources for De ana ne	neeworks. i interp	ie of duality.	(8 hours)
	Module – 2		
Network Theorems: Superposition, Thevenin's, Norton's, and dependent ac and dc sources Two port networks: Open Circuit (Z) parameters, Short Circu and Hybrid parameters, their inter relation	it (Y) parameters,	Transmission (ABCD) p	arameters
	Module – 3		
Transient Analysis and Initial Conditi Initial conditions in network, initial cond series RL, RC and RLC circuit for DC ex circuit for AC excitation,	ons: ditions in basic pa xcitation, transien		-
	Module – 4		
Laplace Transformation and Applicate Definition, Laplace and inverse Laplace Transform, Waveform synthesis, initia Sinusoidal functions. Convolution theor	transforms of stan Il and final value	theorems. Impulse, Ste	-

	Module – 5
Invers	e Laplace Transform
Invers LT, re Trans	e Laplace Transform- using Convolution Integral, solving differential equations using esponse of RL, RC and RLC series circuit to DC excitation and sinusoidal excitation. form of basic R, L and C elements, initial conditions in network analysis. Nodal and analysis in S-domain
-	(8 hours) Network Reduction Techniques, Mesh and Node Analysis, Network theorems, ac circuit s for transients, use of Laplace Transformation and S-domain analysis.
	e outcomes: adents will be able to
	Apply Laplace transformation and Inverse Laplace Transformation techniques for standard functions,
	Analyze DC and AC networks using basic network reduction techniques, Mesh Current and Node Voltage analysis Methods and Network Theorems.
CO3:	Analyze electrical circuits under Transients, with initial conditions and two port parameters.
CO4:	Synthesise waveforms and obtain its Laplace Transform
-	ion paper pattern:
	will be conducted for 100 marks.
	The question paper will be set for 100 marks and marks scored will be proportionally scaled down to 50 marks
•	The question paper will have ten questions. Each question is set for 20 marks. There will be 2 questions from each module. Each of the two questions under a module (with a maximum of 3 sub-questions), should have a mix of topics under that module. The students have to answer 5 full questions, selecting one full question from each module.
CIEA	vill be conducted for 100 marks.
٠	Three Unit Tests each of 20 Marks (Duration 01 hour). Two assignments each of 10 Marks .
•	Two alternate assessment tools (AATs) from the list shall be planned to attain the COs and POs for 20 Marks (duration 01 hour).
	m of three tests, two assignments, and AATs will be out of 100 marks and will be scaled to 50 marks.
Textb	ooks
	Hayt, Kemmerly and Durbin, "Engineering Circuit Analysis", TMH, 6 th Edition, 2002
2.	Ravish R Singh, "Electrical Network", Tata McGrawHill, Edition 2009
Refer	ences
	M.E Van Valkenberg, "Network Analysis", PHI, 3 rd Edition, Reprint 2002
2.	D. Roy Choudhary, "Networks and Systems", 2 nd Edition, New Age international

 D. Roy Choudhary, "Networks and Systems", 2nd Edition, New Age international Publishing,

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING Choice Based Credit System (CBCS) SEMESTER – III					
	cal Machines - I (2:1:0) 3	_			
	om the academic year 2022-2023	-	ſ		
Course Code	21EE36	CIE Marks	50		
Teaching Hours/Week (L:T:P)	2:2:0	SEE Marks	50		
Total Number of Lecture Hours	40	Exam Hours	03		
Course objectives: This course will enable students	s to:				
 Draw the circuit model of Tra Understand Operational chara applications Explain the necessity of starte Study the speed control meth 	acteristics of transformers an er and starting methods of in	nd induction mach	-		
	Module – 1				
Introduction: Role of Electrical machinery in nation's power generation, industrial applications like automation, robotics, electric vehicles etc. Single Phase Transformers: Practical transformer - vector diagram of practical transformer on no load and load, Equivalent Circuit model of a transformer-Approximate and simplified, OC and SC tests- predetermination of Efficiency, Voltage regulation, parameters of equivalent circuit. Three Phase Transformers – Introduction, Constructional features of three phase transformers, choice between single unit and bank of three single phase transformers, Three phase transformer connections – star-star, star- delta, delta-star, delta-delta, open delta (V-V), Comparative features. (8hours) Module – 2 Testing and parallel operation of transformers: Polarity test, Sumpner's test, Parallel operation of transformers – Necessity of Parallel operation, conditions for parallel operation - single phase and three phase, Load sharing in case of similar and dissimilar transformers.					
transformers (Cu- Saving). Tap c transformers.	hanging transformers: No	load and on load	tap changing		
			(8hours)		
	Module – 3				
Three phase Induction Motor - I speed of rotor field, rotor EMF, rot rotor torque, Torque - slip curve, parameter variation on torque resistances, stator voltage, freque Losses and power flow in three synchronous watt. Equivalent cor relation between rotor input and motor. Comparison of three phase of three phase Induction motors	tor current and power factor starting torque, Full load to speed characteristics (Va ncy). phase Induction motor- ro ircuit model - Electrical ed d rotor cu-loss, Phasor diag	Rotor Torque - Ex orque, pull out torc ariation of stator tor output and m quivalent of mech gram of three pha	xpression for que. Effect of and rotor otor torque, nanical load, se Induction		

Module – 4

(8 Hours)

Testing and performance of Three Phase IM - No load test, blocked rotor test. Circle diagram – construction and predetermination of performance (efficiency, slip, torque, power factor, and current, at any given load and at maximum conditions), factors affecting performance of three phase Induction motor, cogging and crawling.

(8hours)

Module – 5

Single Phase Induction Motors

Constructional features double revolving field theory, equivalent circuit, determination of parameters, Split-phase starting methods and applications.

Recap: Emphasis on fundamental concepts of electrical machines, principles, circuit models and applications such as robots and electric vehicles.

(8 hours)

Course outcomes:

The students will be able to:

- CO1: Describe the operating principles and characteristics of Transformers and Induction Motors
- CO2: Describe the methods of testing and estimate performance of Transformers and Induction motors
- CO3: Analyze the operation of Transformers and Induction Motors using phasor diagrams and circuit model of machines

Question paper pattern:

SEE will be conducted for 100 marks.

- The question paper will be set for 100 marks and marks scored will be proportionally scaled down to 50 marks
- The question paper will have ten questions. Each question is set for 20 marks.
- There will be 2 questions from each module. Each of the two questions under a module (with a maximum of 3 sub-questions), should have a mix of topics under that module.
- The students have to answer 5 full questions, selecting one full question from each module.

CIE will be conducted for 100 marks.

- Three Unit Tests each of **20 Marks** (Duration 01 hour).
- Two assignments **each of 10 Marks**.
- Two alternate assessment tools (AATs) from the list shall be planned to attain the COs and POs for **20 Marks** (duration 01 hour).
- The sum of three tests, two assignments, and AATs will be out of 100 marks and will be scaled down to 50 marks.

Textbooks:

- 1. D. P Kothari, "Electrical Machines", McGraw Hill, 4th edition 2011
- 2. Dr. P.S. Bhimbra, "Electrical Machinery", Khanna Publications, 7th Edition, 2007
- 3. AbhijitChakrabarti, "Electric Machines", McGraw Hill, Education 2015

References:

- 1. Ashfaq Husain, "Electric Machines", Dhanpat Rai and Co., Second Edition, 2014
- 2. M. G. Say, "Performance and Design of Alternating Current Machines", John Wiley and Sons Publications, 3rdEdition,2002
- 3. A S Langsdorf, "Alternating current Machines", McGraw Hill, Education -1984

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING

Choice Based Credit System (CBCS)

SEMESTER – III

Analog Electronic Circuits (2:1:0) 3 (Effective from the academic year 2021 -2022)							
Course Code21EE37CIE Marks50							
Teaching Hours/Week (L:T:P) 2:2:0 SEE Marks 50							
Total Number of Lecture Hours40Exam Hours3 Hours							
Course objectives:							

This course will enable students to

- 1. Design diode-based wave shaping circuits.
- 2. Understand biasing techniques and stability concepts.
- 3. Understand transistor modelling fundamentals and frequency response
- 4. Understand feedback concepts.
- 5. Understand power amplifier and oscillator circuits.
- 6. Design oscillator and amplifier circuits.

Module – 1

INTRODUCTION: Relevance of electronics in the Global scenario. Role of electronics in environmental and Societal concerns. Internship and Job opportunities. Significance and application of the course in Electrical and Electronics Engineering.

a. Diode applications:

Diode Clippers: Operation of series clippers and parallel clippers with and without biasing. **Diode Clampers:** Operation of positive and negative clampers with and without biasing.

b. Bipolar Junction Transistor (BJTs): -Introduction to Transistor Biasing, Operating point, Emitter Bias, Collector feedback bias, Voltage divider Bias configuration. Stability factors of Voltage Divider Bias configuration. Problems on Biasing, stability factors, and design operations (Voltage divider bias only).

Hours)

Self- study: Simulation of Rectifier circuits and Clippers and clampers.

Module – 2
BJT Transistor Modelling: re-model for CE and CB configuration. Derivation of Hybrid model.
Determination of performance parameters such as input and output impedances, voltage and
current gains using re and hybrid model (based on voltage divider bias and Emitter Bias only).
Emitter follower circuit: Operation and analysis. Two port network model of a transistor. CB
configuration. Analysis of Darlington emitter follower circuit. Problems on re-model and h-
model for voltage divider bias and emitter bias configuration only. (8
Hours)

Module – 3

a. BJT Frequency Response:-Introduction to frequency response, Low-frequency response, and High-frequency response of BJT Amplifier (Qualitative treatment only). Miller effect Capacitance. **Multistage Amplifiers:** Cascaded systems. Determination of overall voltage gain and current gain in a cascaded system. Study of loading effect on cascaded connections. Problems on cascaded systems.

b. Feedback Amplifier: Introduction to feedback amplifiers, Feedback concept, Feedback connections type. Trans-conductance, Trans-resistance, Voltage, and Current Amplifier analysis in terms of Input and output impedances.

Hours)

Module – 4

a. Power Amplifiers: Introduction to power Amplifiers, Classification of power amplifiers. Class A Amplifier: Series Fed and Transformer coupled, DC Bias Operation, AC operation, Power Consideration, Efficiency. Class B operation: Class B Amplifier Circuits- Transformer coupled Push- Pull Circuits, Complementary Symmetry Circuits expression for efficiency. Problems on Class A operation only.

b. Oscillators: Introduction to oscillator, Barkhausen criteria, Operation of RC Phase shift oscillator, Wein bridge oscillator, Hartley oscillator, Colpitts oscillator, Crystal oscillator. Frequency expression of various oscillators (No derivation)

Simple oscillator design problems for RC Phase shift oscillator, Wein bridge oscillator, Hartley oscillator, Colpitts oscillator, Crystal oscillator. (8

Hours)

Module – 5

FET Amplifiers :

Review of FET and MOSFET construction and characteristics (no questions are to be set from this topic)

a. JFETs: Derivation of trans-conductance. FET parameters. Biasing of FET:Common source configuration with voltage divider bias.

JFET Amplifiers: FET AC equivalent circuit, JFET Voltage Divider Configuration (with bypass capacitor only). Finding input and output impedances and voltage gain.

b. MOSFETS: Small signal model of Depletion type MOSFET and Enhancement type MOSFET.
 Finding input impedance, output impedances and voltage gain of MOSFET amplifier circuits with voltage divider bias (with bypass capacitor only).

Hours)

Course outcomes:

The students will be able to:

CO1: Explain the operation of various solid-state devices and circuits.

CO2: Apply the knowledge of network theorems to determine the bias point details.

CO3: Design wave shaping and signal conditioning circuits using solid state devices(Diodes and Transistors).

CO4: Analyze the performance parameters of various amplifier and oscillator circuits. CO5: Apply an appropriate modern tool to simulate electronic circuits.

Question paper pattern:

SEE will be conducted for 100 marks.

- The question paper will be set for 100 marks and marks scored will be proportionally scaled down to 50 marks
- The question paper will have ten questions. Each question is set for 20 marks.
- There will be 2 questions from each module. Each of the two questions under a module (with a maximum of 3 sub-questions), should have a mix of topics under that module.
- The students have to answer 5 full questions, selecting one full question from each module.

CIE will be conducted for 100 marks.

- Three Unit Tests each of **20 Marks** (Duration 01 hour).
- Two assignments **each of 10 Marks**.
- Two alternate assessment tools (AATs) from the list shall be planned to attain the COs and POs for **20 Marks** (duration 01 hour).
 - The sum of three tests, two assignments, and AATs will be out of 100 marks and will be scaled down to 50 marks.

Textbooks:

1. Robert L.Boylestad and Louis Nashelsky, "Electronic Devices and Circuit Theory", PEARSON EDUCATION, 11th edition

References:

- 1. David A Bell, Electronic Devices and Circuits, PHI, 5th edition.
- 2. Millman and Halkias, TMH, Electronic Devices and Circuits, 4th edition.
- 3. Charles Roth Jr.LarryL.Kinney, Raghunandan G.H, "Fundamentals of Analog and digital electronics", Cengage Learning, 2019

B.E. ELECTRICAL AND ELECTRONICS ENGINEERING Choice Based Credit System (CBCS)			
Floatrical	SEMESTER – III	(0.0.1) 1	
	Machines Laboratory - I e from the academic year 2021		
Course Code	21EEL38A	CIE Marks	50
Teaching Hours/Week (L:T:P)	0:1:2	SEE Marks	50
Total Number of Lecture Hours	30	Exam Hours	3
Course objectives: This course will enable students to 1. Conduct open and short circuit 2. Conduct load tests on single pl 3. Conduct test on induction mot 4. Conduct Scott connection test 5. Conduct speed control test on List of Experiments	t tests on single phase tra nase and three phase indu or to determine the perfo on transformers	action motor. Armance characteristics	
 No Load and blocked rotor test of Parallel operation of two dissimulation Sumpner's test Three phase connection of three Scott connection for balanced ar Load test on single phase induct Load test on three phase inducti Comparison of performance of d Equivalent circuit analysis of transition Speed control of three phase induct 	ilar single phase transformed single phase transformed ad unbalanced two phase ion motor on motor lirect and indirect load tea nsformer with indirect te	mers. rs -Start-Delta. UPF loads. st on transformer sts	
2. Three phase connection of three	single phase transformer	rs of Delta -Delta and O	pen Delta.
Course outcomes: The students will be able to:			
CO1: Determine the performance c CO2: Determine the performance c CO3: Analyze the load sharing of t	haracteristics of three ph	ase Induction motors	

Choice	AND ELECTRONICS ENGIN Based Credit System (CBCS)	IEERING	
	SEMESTER – III		
	ectronics Laboratory (0:0:1) 1		
Course Code	from the academic year 2021-22) 21EEL38B	CIE Marks	50
Teaching Hours/Week (L:T:P)	0:1:2	SEE Marks	50
Total Number of Contact Hours	30	Exam Hours	3
Course objectives:	·		
This course will enable students to:			
1. Design Clipper and Clamper Ci			
2. Design Amplifier for a given se	-		
3. Analyse power amplifier circu	it.		
4. Design Oscillator circuits.	nlifion circuita		
5. Plot frequency response of am List of Experiments	pinier circuits.		
1. Design and Testing of diode cli	ipping circuits		
2. Design and Testing of diode cla	11 0		
3. Design and Testing of Full way			
	's of a transistor in CE configura	tion.	
-	C coupled amplifier with simula		
6. Design and testing of BJT as R			
7. Performance analysis of class			
-	ven set of specification-Hardwa	re/Simulation	
9. Implementation of a transistor	-		
10. Frequency response characte		ollower circuit	with the
determination of input and ou	-	Shower cheult,	with the
Open Ended Experiment	tput impedances.		
1. Lead Compensators.			
2. Lag Compensators.			
3. Testing of a MOS amplifier circ	cuit-Hardware/Simulation		
	g to determine the different cu	irrents and volt:	ages of a
given biasing network.			
Course outcomes:			

The students will be able to:

CO1: Design and test wave-shaping circuits.

CO2: Design and Test Transistor amplifier and oscillator circuits.

CO3: Apply an appropriate modern tool to simulate electronic circuits.

		ND ELECTRONICS E Based Credit System (CBCS)		
		SEMESTER – III)	
	Electrical Si	mulation Laboratory		
		om the academic year 2021-		
	rse Code	21EEL38C	CIE Marks	50
	hing Hours/Week (L:T:P)	0:1:2	SEE Marks	50
	l Number of Contact Hours	30	Exam Hours	3
1.	e objectives: To impart practical working knowl using Mathematical computing lang To Solve, Simulate and Analyse bas writing Mathematical Equations an To develop hands on working expe Electrical & Electronics Circuits usi	guages such as MATLAB. tic Electrical and Electroni ad Programs. trience with reference to S	ics Circuits and Applic	cations by
	st of Experiments			
1.	Basic operations on matrices			
2.	Simulate the circuit to find out the	e node voltages and respe	ctive branch currents	
3.	Series and parallel resonance			
4.	Generation of Various Signals suc sinusoidal, ramp, sinc.	ch as Unit impulse, unit s	step, square, saw too	th, triangular
5.	Measurement of Active power of	three phase circuit for bal	lanced and unbalance	ed loads
6. 7.	Finding the fourier transform of a Verification of Network Theorem theorem and Maximum power Tra	ns- Superposition theore		-
8.	Study of Single Phase Half Wave		R and RL Load	
9.	Study of Single Phase Bridge Con			
10.	Study of Load and Load Duration			
11.	Locating the zeros and poles and given transfer function	plotting the pole zero m	aps in s-plane and z	-plane for the
12.	Harmonic analysis of non-sinusoid	dal waveforms		
13.	Three Phase waveform generation	using MATLAB, Simul	ink and Simscape	
14.	Measurement of power and power	factor of single phase R	LC circuit.	
	rse Outcomes: On successful com	-		
CO1	: Understand the main features and environment.	importance of the MATLA	B mathematical progra	amming
CO2			oolbox to simulate a	and solve
CO3				

	AND ELECTRONICS Based Credit System (CBC SEMESTER – III		
Dinloma	Mathematics- I (0:0:0)	NII	
-	MON TO ALL BRANCHES		
	om the academic year 20		
Course Code	21DIP31A	CIE Marks	100
Teaching Hours/Week (L:T:P)	3:0:0	SEE Marks	-
Total Number of Contact Hours	30	Exam Hours	3
Course Objectives: This course will enable students to			0
 To enable students to apply fields by making them to lear elementary Linear Algebra. 	-		
2. To familiarize the important analyze the engineering prob		Integral Calculus re	equired to
	Module – I		
Calculus, Linear algebra and its ap Economics. Differential Calculus-I: Differentia and tangent, angle between two cu single variable.	ation: Polar curves: ang	le between the rad oblems; Maclaurin's	ius vector
6	Module – II		()
Differential Calculus-II: Partial composite functions, Jacobians-simp	•		tiation of (6 Hours)
	Module – III		
Vector Differentiation: Velocity a Scalar and vector point functions. G and irrotational vector fields-Proble	radient, Divergence, Cu		
Linear Algebra: Introduction - Ran		ary row operations	- Echelon
form. Gauss elimination method and values and Eigen vectors of a square (6 hours)	approximate solution b	y Gauss-Seidel met	hod. Eigen
<u> </u>	Module – V		
Integral Calculus: Reduction form	ulae for $\int Sin^n x dx$,	$\int Cos^n x dx$ (p)	roofs with
limits between 0 and $\pi/2$), \int	<i>Sin^mxCosⁿxdx</i> (m & n z	are positive intege	rs) (proof
without limits) and problems on the		with limits. Double	and triple
integration-Simpleexamples. of the Course.		(6 hours)	'Summary
Course outcomes:		(0 nours)	
The students will be able to:			
CO1: Use derivatives to calculate rat	e of change of functions	of a single and mul	tivariate
variable.			

CO2: Analyze position, velocity and acceleration in two and three dimensions of vector Valued functions.

CO3: Learn techniques of integration including the evaluation of double and triple integrals.

CO4: Solve system of Linear equations by using Matrix Algebra.

Question paper pattern:

CIE will be conducted for 100 marks.

- Three Unit Tests each of **20 Marks** (Duration 01 hour).
- Two assignments each of 10 Marks.
- Course Seminar suitably planned to attain the COs and POs for 20 Marks (duration 01 hours).
- The sum of three tests, two assignments, and a seminar will be out of 100 marks. The student shall secure a minimum of 40% of marks of the course to qualify and become eligible for the award of a degree.

Textbooks:

- **1.** B.S. Grewal, Higher Engineering Mathematics, 43rd Edition, Khanna Publishers, 2015.
- **2.** B.V. Ramana, Higher Engineering Mathematics, Tata McGraw-Hill, 2010.
- **3.** C. Pandurangappa, Advanced Mathematics II (Lateral entry bridge course textbook), 3rd Edition, Sanguine Publishers, 2015.

References:

- 1. N.P. Bali, Manish Goyal, A Text Book of Engineering Mathematics, Laxmi Publishers, 2014.
- **2.** E. Kreyszig, Advanced Engineering Mathematics, 10th Edition, John Wiley & Sons, 2015.
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