DEPARTMENT OF
ELCTRONICS AND COMMUNICATION ENGINEERING
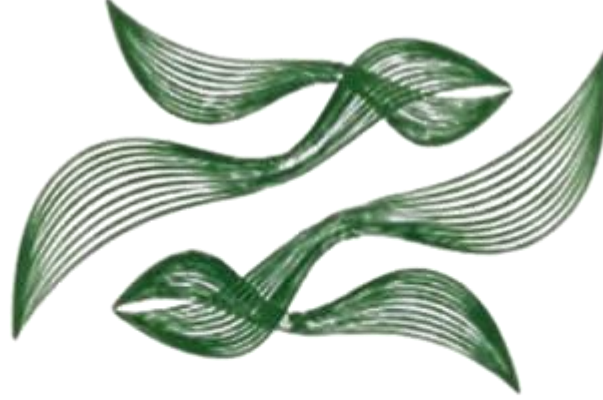
Report on

SILICON SYNERGY-2025

An Industry-Academia conclave

About the Event:

Silicon Synergy 2025 was a significant industry-academia conclave hosted by the BMS Institute of Technology and Management (BMSIT&M) in Yelahanka, Bengaluru, on April 24, 2025. Organized by the PRIME Club of BMSIT&M, the event aimed to foster collaboration between academia and industry in the fields of semiconductors, embedded systems, and emerging technologies.

The event facilitated knowledge exchange, networking, and discussions on innovation and entrepreneurship, aligning with BMSIT&M's commitment to fostering engineering excellence.
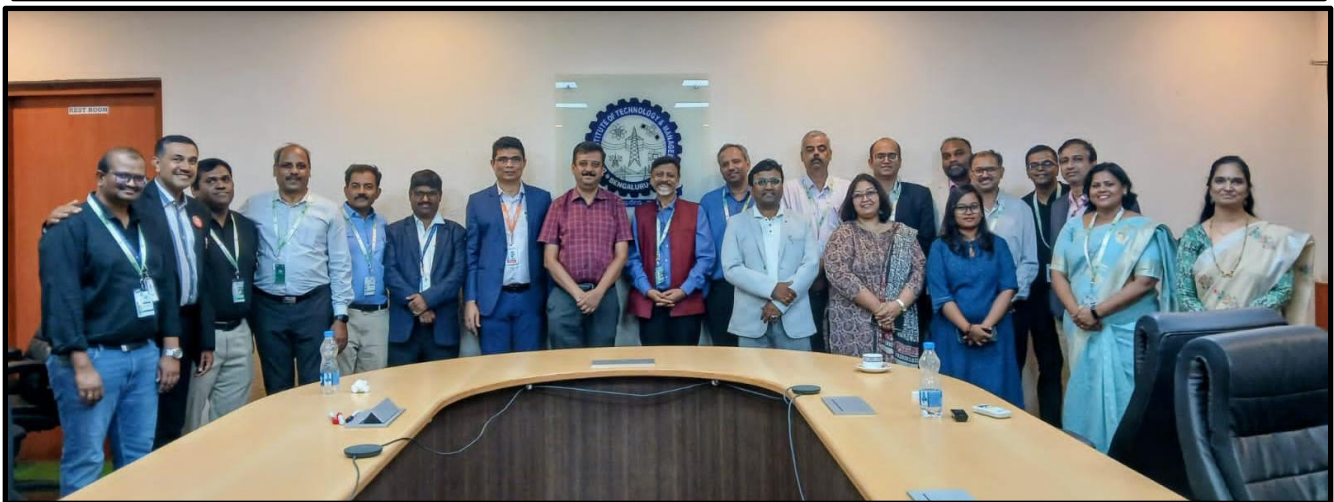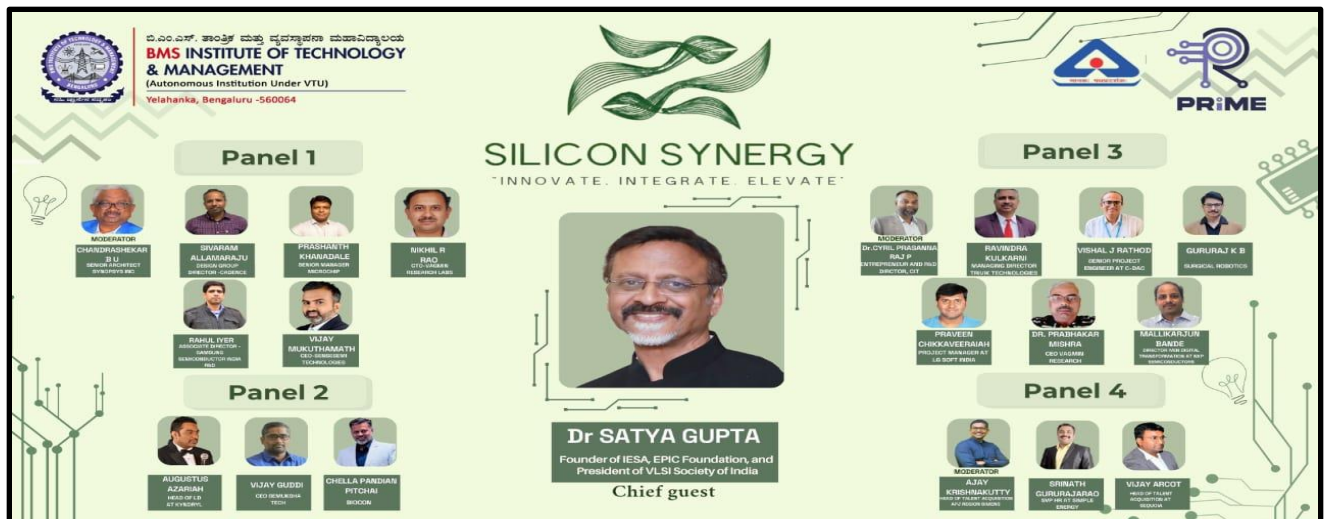
Objectives of the Event:

1. **Bridge the Industry-Academia Gap:**

   Foster collaboration between academic institutions and the **semiconductor /embedded** systems industries to align educational outcomes with real-world applications.

2. **Promote Innovation & Research:**

   Encourage students and faculty to engage in research, innovation, and development in VLSI, embedded systems, and semiconductor technologies.

3. **Enable Knowledge Exchange:**

   Provide a platform for thought leaders, industry experts, and academicians to share insights on the latest trends, challenges, and opportunities in the tech ecosystem.

4. **Enhance Skill Development:**

   Equip students with industry-relevant knowledge and soft skills through exposure to panel discussions, expert talks, and networking sessions.

5. **Foster Entrepreneurship & Startups:**

   Motivate young engineers to explore entrepreneurial ventures and understand the startup landscape in electronics and embedded domains.

6. **Encourage Campus-Industry Connect:**

   Build strategic partnerships for internships, placements, and collaborative projects between BMSIT&M and leading tech companies.

7. **Inspire Future Engineers:**

   Instill a spirit of curiosity, innovation, and lifelong learning among students to shape them into competent professionals.

Event Details:

Date and Time	Thursday, April 24, 2025, at 9:30 AM
Venue	BMSIT&M, Yelahanka, Bengaluru
Organized By	PRIME Club, BMSIT&M
Theme	"Innovate. Integrate. Elevate."

Distinguished Guests:

Chief Guest:

Dr. Satya Gupta	● Founder of IESA and EPIC Foundation ● President of VLSI Society of India

Sri Aviram Sharma	Trustee, BMSET; Chairman, BOG, BMSIT&M & BMSSA
Dr. B. S. Ragini Narayan	Chairperson, Donor Trustee & Member Secretary, BMSET
Dr. P. Dayananda Pai	Life Trustee, BMSET; Chairman, BOG, BMSET
Dr. Thirumalachari Ramasami	Trustee, BMSET
Dr. Sanjay H. A	Principal, BMSIT&M
Dr. Anil G. N	Vice Principal, BMSIT&M

Speakers & Panelists:

Chandrashekhar B U	Synopsys Inc.
Sivaram allamaraju	Cadence
Prashanth khanadale	Microchip
Nikhil R Rao	Vagmin research labs
Rahul iyer	Samsung semiconductors
Vijay mukuthamath	Sensesemi Technologies
Augustus azariah	IBM
Vijay guddi	Semi-ksha semiconductors
Chella pandian	Biocon
Cyril Prasanna Raj P	Cambridge institute of technology
Ravindra kulkarni	Trivik Technologies
Vishal J Rathod	C-DAC bangalore
Praveen chikkaveeraiah	LG india
Dr. Prabhakar Mishra	Vagmin research labs
Gururaj K B	Comofi medtech
Mallikarjun Bande	NXP semiconductors
Ajay Krishnakutty	Siemens Healthineers
Srinath Gururajrao	Simple Energy
Vijay Arcot	Sequoia
Suraj Chettri	Airbus india
Satish M Bhavankar	TIIEC
Suchandra Dutta	Alif Semiconductors
Muthukumaran S	Tessolve

Event Highlights:

Panel 1: The Semiconductor Renaissance

This panel focused on the resurgence of the semiconductor industry, exploring how global advancements in technology are propelling innovations in semiconductors. Industry experts discussed the critical role that semiconductors play in emerging technologies such as Artificial Intelligence, 5G, automotive electronics, and more. The conversation highlighted how India, in particular, can strengthen its position in the global semiconductor market by improving R&D, fostering industry-academia collaboration, and developing a skilled workforce to meet future demands.

Panel 2: Campus Connect: Industry-Academia Partnership

The "Campus Connect" panel emphasized building strong bridges between academic institutions and the tech industry. Key discussions centered around updating engineering curricula to keep pace with rapidly changing technological landscapes. Panelists shared best practices for creating symbiotic relationships between universities and industries, such as offering internships, research collaborations, and hands-on projects. The session aimed to help students gain practical exposure and ensure that they are industry-ready upon graduation.

Panel 3: The Future of Embedded Systems

The "Future of Embedded Systems" panel explored the pivotal role embedded systems play in various industries, particularly in areas such as IoT, automotive, healthcare, and industrial automation. Experts shared insights into how embedded technologies are evolving, especially with the rise of smart devices and connected systems. The discussion highlighted the potential for embedded systems to drive major technological advancements and transform industries by making them more efficient, intelligent, and sustainable.

Panel 4: **From Breadboard to Design Desk**

This session provided an in-depth look into the journey of taking an idea from a physical prototype (breadboard) to a fully developed and manufactured product. Industry leaders and experts discussed the challenges of the product development lifecycle, from design and simulation to validation and testing. The panel also touched on design thinking, ensuring that students and professionals gain an understanding of how conceptual designs evolve into practical, market-ready solutions. This discussion was invaluable for anyone looking to understand the real-world application of hardware design and product prototyping.

Media Presence:

EDUCATION HUB

BMS Institute of Technology and Management conducted "Silicon Synergy 2025" a Semiconductor Industry Conclave and Expo

Silicon Synergy is a premier semiconductor industry conclave and expo oraganised in association with Bureau of Indian Standards(BIS).

Event aims to foster collaboration between Academia and Semiconductor Industry. Part of the event was: Keynote address by Dr. Satya Gupta, 4 Technical Panels with 20+ experts and Industrial Product Exhibitions. This event is a gateway to the staff and students to get an insight on industry trends, exposure and R&D collaborations. During the inauguration of the event the Chief Guest Dr. Satya Gupta, Founder of IESA, EPIC Foundation, and President of VLSI Society of India, Mr. Sivaram Allamaraju, Design Group Director, Cadence and, Mr. Chandrashekar B U, Senior Architect, Synopsys, Principal Dr. Sanjay H A, Vice Principal Dr. Anil G N, Event coordinator Dr.Raju Hajare, Dr.Manjunath T N, Dean Career Guidance and student coordinators were present. The Chief Guest, Dr. Satyagupta, delivered a thought-provoking Keynote Address, emphasizing the need for academic institutions to continuously evolve and align with industry trends. Principal, Dr. Sanjay H A spoke about the institution's mission to create not just engineers, but visionaries – equipped with the skills, mindset, and confidence to make a difference in society. The panel focused on India's growing footprint in the global semiconductor industry. Experts discussed how innovation, policy support, and talent development are aligning to make India a major semiconductor hub. The heart of Silicon Synergy 2025 lay in its vibrant exhibition arena, featuring a diverse and dynamic range of industry, academic, and experiential stalls. Attendees engaged with experts, explored job and internship opportunities, and experienced real-world applications of electronics and embedded systems. These stalls showcased breakthroughs in semiconductors, embedded systems, AI, power electronics, and next-gen solutions.

ವಿಜಯವಾಣಿ ನಂ.1

ಸೆಮಿಕಂಡಕ್ಟರ್ ಉದ್ಯಮ ಸಮಾವೇಶ ಯಶಸ್ವಿ
ಬಿಎಂಎಸ್ಐಟಿ ಯಲಹಂಕದಲ್ಲಿ ಕಾರ್ಯಕ್ರಮ | ವಿವಿಧ ಕಂಪನಿ ಉತ್ಪನ್ನಗಳ ಪ್ರದರ್ಶನ

■ ವಿಜಯವಾಣಿ ಸುದ್ದಿಜಾಲ ಬೆಂಗಳೂರು

ಬ್ಯೂರೋ ಆಫ್ ಇಂಡಿಯನ್ ಸ್ಟಾಂಡರ್ಡ್ಸ್ ಸಹಯೋಗ ದಲ್ಲಿ ಯಲಹಂಕದ ಬಿಎಂಎಸ್ಐಟಿ ಕಾಲೇಜಿನಲ್ಲಿ ಹಮ್ಮಿಕೊಂಡಿದ್ದ ಸೆಮಿಕಂಡಕ್ಟರ್ ಉದ್ಯಮ ಸಮಾವೇಶ 'ಸಿಲಿಕಾನ್ ಸಿನರ್ಜಿ' ಯಶಸ್ವಿಯಾಗಿ ಜರುಗಿತು.

ವಿವಿಧ ಕಂಪನಿಗಳು ತಾವು ಅಭಿವೃದ್ಧಿಪಡಿಸಿದ ಎಲೆಕ್ಟ್ರಾನಿಕ್ಸ್ ಉತ್ಪನ್ನಗಳನ್ನು ಪ್ರದರ್ಶಿಸಿದರು. ಸ್ಮಾರ್ಟ್ ರೋಬಾಟ್, ಎಂಬೆಡೆಡ್ ಚಿಪ್ಸ್, ಸ್ಮಾರ್ಟ್ ಪ್ಯಾನೆಲ್ ಗಳಂತಹ ಆವಿಷ್ಕಾರಗಳು ವಿದ್ಯಾರ್ಥಿಗಳ ಉತ್ಸಾಹವನ್ನು ಇಮ್ಮಡಿಗೊಳಿಸಿತು. ಸಮಾವೇಶದಲ್ಲಿ ಸೆಮಿಕಂಡಕ್ಟರ್ ಉದ್ಯಮ, ಅವಕಾಶಗಳ ಬಗ್ಗೆ ಸಂವಾದ ನಡೆಸಲಾಯಿತು. ಸುಮಾರು 20 ಉದ್ಯಮಿಗಳು ಭಾಗವಹಿಸಿ ವಿದ್ಯಾರ್ಥಿಗಳ ಪ್ರಶ್ನೆಗಳಿಗೆ ಸಮರ್ಪಕವಾಗಿ ಉತ್ತರಿಸಿದರು.

ಸಮಾವೇಶ ಉದ್ಘಾಟಿಸಿ ಮಾತನಾಡಿದ ವಿಎಲ್ಎಸ್ಐ ಸೊಸೈಟಿ ಆಫ್ ಇಂಡಿಯಾ ಅಧ್ಯಕ್ಷ ಡಾ. ಸತ್ಯಗುಪ್ತ, ಭಾರತ ಕೇವಲ ಉತ್ಪಾದನಾ ರಾಷ್ಟ್ರವಾಗದೆ ಹೊಸ ತಂತ್ರಜ್ಞಾನ ಮತ್ತು ಸೆಮಿಕಂಡಕ್ಟರ್ ಉತ್ಪನ್ನಗಳ ಆವಿಷ್ಕಾರವನ್ನು ಮಾಡುವ ದೇಶವಾಗಬೇಕು. ಆಗ ಮಾತ್ರ ಭಾರತ ಕ್ಷಿಪ್ರ ಪ್ರಗತಿಯಲ್ಲಿ ಪ್ರಗತಿ ಕಾಣಲು ಸಾಧ್ಯ ಎಂದು ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ಕರೆ ನೀಡಿದರು.

2047ರಲ್ಲಿ ಸೆಮಿಕಂಡಕ್ಟರ್ ಉದ್ಯಮ ಸುಮಾರು 120 ಬಿಲಿಯನ್ ಡಾಲರ್ಗಳಷ್ಟು ಚಿಪ್ಗಳನ್ನು ಭಾರತದಲ್ಲಿಯೇ

ಯಲಹಂಕ ಬಿಎಂಎಸ್ಐಟಿ ಕಾಲೇಜಿನಲ್ಲಿ ಹಮ್ಮಿಕೊಂಡಿದ್ದ ಸೆಮಿಕಂಡಕ್ಟರ್ ಉದ್ಯಮ ಸಮಾವೇಶದಲ್ಲಿ ಸಾಧಕರನ್ನು ಸನ್ಮಾನಿಸಲಾಯಿತು. ಸತ್ಯಗುಪ್ತ, ಶಿವರಾಂ ಅಲ್ಲಮ ರಾಜು, ಡಾ. ಸಂಜಯ್ ಇತರರಿದ್ದರು.

ಉತ್ಪಾದಿಸಲಿದೆ. ಅದನ್ನು ಸಾಕಾರಗೊಳಿಸುವ ಹೊಣೆ ವಿದ್ಯಾರ್ಥಿಗಳದ್ದು. ಈ ಉದ್ಯಮದಲ್ಲಿ ಉತ್ತಮ ಸಂಬಳ ಕೂಡ ದೊರೆಯಲಿದೆ. ವಾರ್ಷಿಕ 30 ಸಾವಿರ ಸೆಮಿಕಂಡಕ್ಟರ್ ಇಂಜಿನಿಯರ್ಗಳ ಬೇಡಿಕೆ ಇದೆ. ಸದ್ಯ ಅಷ್ಟು ನುರಿತ ಇಂಜಿನಿಯರ್ಗಳು ದೊರೆಯುತ್ತಿಲ್ಲ. ಕೃತಕ ಬುದ್ಧಿಮತ್ತೆ ಬಳಸಿಕೊಂಡು ಅದರ ಕಾರ್ಯಕ್ಷಮತೆಯನ್ನು ಮತ್ತಷ್ಟು ಹೆಚ್ಚಿಸ ಬಹುದು. ಈ ಸೆಟ್ಟಿನಲ್ಲಿ ಯುವ ಪದವೀಧರರು

ಯೋಚಿಸಬೇಕು ಎಂದರು.

ಸಿನೋಪ್ಸಿಸ್ ಸಂಸ್ಥೆಯ ಸೀನಿಯರ್ ಆರ್ಕಿಟೆಕ್ಟ್ ಚಂದ್ರಶೇಖರ್, ಕ್ಯಾಡೆನ್ಸ್ ಡಿಸೈನ್ ಗ್ರೂಪ್ ನಿರ್ದೇಶಕ ಶಿವರಾಂ ಅಲ್ಲಮ ರಾಜು, ಪ್ರಾಂಶುಪಾಲ ಡಾ. ಸಂಜಯ್ ಎಚ್.ಎ., ಉಪ ಪ್ರಾಂಶುಪಾಲ ಡಾ. ಅನಿಲ್ ಜಿ.ಎನ್., ಡೀನ್ ಕರಿಯರ್ ಗೈಡೆನ್ಸ್, ಡಾ. ಟಿ.ಎನ್. ಮಂಜುನಾಥ್, ಮುಖ್ಯ ಸಂಯೋಜಕ ಡಾ. ರಾಜು ಹಜಾರೆ ಉಪಸ್ಥಿತರಿದ್ದರು.

BMSITM Bridges Academia and Industry in Semicon Conclave

From Left to Right: Ananth Mallya, Chief Advisor, Strategic Industry Liaison, BMSIT&M; Dr.Raju Hajare, Professor, ECE, BMSIT&M; Dr. Sanjay H A, Principal, BMSIT&M; Sivaram Allamraju, Design Group Director, Cadence; Dr Satya Gupta, Corporate- Intel President, VLSI Society of India (Sitting); Chandrashekhar B U, Senior Architect, Synopsys Inc; Dr Anil G N, Vice - Principal, BMSIT&M; Dr. Manjunath T N, Dean, Career Guidance, BMSIT&M; and Saichandan Anagani, student, ECE, BMSIT&M

BMS Institute of Technology and Management recently hosted "Silicon Synergy 2025," a premier Semiconductor Industry Conclave and Expo held in association with the Bureau of Indian Standards (BIS).

The event aimed to strengthen collaboration between academia and the semiconductor industry. Highlights included a keynote address by Dr. Satya Gupta, four expert-led technical panels featuring 20+ industry leaders, and an industrial product exhibition.

Silicon Synergy served as a valuable platform for students and faculty to gain insights into emerging industry trends, explore R&D opportunities, and connect with key stakeholders in the semiconductor ecosystem.