



BMS INSTITUTE OF TECHNOLOGY AND MANAGEMENT

YELAHANKA - BANGALORE - 64

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

MASTER TIME TABLE FOR THE ACADEMIC YEAR 2017 - 18

DAY	SEMESTER/ SECTION	8.30 - 9.30	9.30 - 10.30	10.30 - 10.55	10.55 - 11.55	11.55 - 12.50	12.50 - 1.45	1.45 - 2.40	2.40 - 3.35	3.35 - 4.30	
MONDAY	III A	AE	NA	Tea Break	MAT	Lunch Break	EEM	AEC/DE lab			
	III B	EEM	MAT		NA		AE	EI	DE	Tutorial	
	V A	Open Elective	HDL		DSP		ITC	DSP/HDL LAB			
	V B	Open Elective	DSP		ITC		M&E	HDL	Tutorial	Tutorial	
	VII A	PE	CCN		DSPA/ANN		OFC	Project	Co-Curricular Activities		
	VII B	OFC	ESD		DSPA/ANN		CCN	PE	Co-Curricular Activities		
TUESDAY	III A	NA	9:30to 9:55 Tea Break	AEC/DE lab 9:55 to 12:50			Lunch Break	MAT(T)	MAT(DIP)	MAT(DIP)	
	III B	EI	EEM	AE	Lunch Break	DE	MAT(T)	MAT(DIP)	MAT(DIP)		
	V A	Professional Elective	Open Elective	Tea Break	DSP/HDL LAB			Lunch Break	M&E	Tutorial	
	V B	Professional Elective	Open Elective		DSP	Lunch Break	ITC	DSP/HDL LAB			
	VII A	CCN	ESD		PE		DSPA/ANN	OFC	IP/RTS	Extra- Curricular	
	VII B	CCN	OFC		ESD		DSPA/ANN	PE	IP/RTS	Extra- Curricular	
WEDNESDAY	III A	EEM	EI		DE		Lunch Break	AE	NA	Co-Curricular Activities	
	III B	MAT	AE		AEC/DE lab			Lunch Break	Co-Curricular Activities		
	V A	M&E	Professional Elective	ITC	Lunch Break	DSP	Tutorial	Co-Curricular Activities			
	V B	DSP	Professional Elective	DSP/HDL LAB			Lunch Break	Co-Curricular Activities			
	VII A	DSPA/ANN	CCN	IP/RTS	Lunch Break	ESD	VLSI/PE LAB				
	VII B	DSPA/ANN	PE	IP/RTS		CCN	OFC	Project	Project		
THURSDAY	III A	AE	MAT	Tea Break	NA	Lunch Break	EI	EEM	DE	Tutorial	
	III B	AE	DE		MAT		NA	AEC/DE lab			
	V A	ITC	HDL		Open Elective		Professional Elective	DSP	PBL		
	V B	ITC	M&E		Open Elective		Professional Elective	HDL	PBL		
	VII A	IP/RTS	OFC		VLSI/PE LAB			Lunch Break	PE	Project	
	VII B	IP/RTS	CCN		ESD		Lunch Break	OFC	VLSI/PE LAB		
FRIDAY	III A	EI	MAT	Tea Break	EEM	Lunch Break	DE	Club Activity		Proctor	
	III B	NA	MAT		EI		EEM	Club Activity		Proctor	
	V A	DSP	HDL		M&E		Extra- Curricular	Club Activity		Proctor	
	V B	M&E	DSP		HDL		Extra- Curricular	Club Activity		Proctor	
	VII A	OFC	ESD		PE		CCN	Club Activity		Proctor	
	VII B	ESD	Tea Break 9:30 To 9:55		VLSI/PE LAB 9:55 To 12:55			Lunch Break	Club Activity		Proctor
SATURDAY	III A	MAT	DE	Tea Break	AE	EI					
	III B	DE	NA		EEM	EI					
	V A	HDL	ITC		M&E						
	V B	HDL	M&E		ITC						
	VII A	ESD	IP/RTS		DSPA/ANN						
	VII B	PE	IP/RTS		DSPA/ANN						

CLASS ROOMS: 1 G - CR 104 | 1 H - CR 105 | 1 I - CR 205 | 3A - CR 406 | 3B - BSN CR 204 | 5A - BSN CR 503 | 5B - BSN TR 502 | 7A - CR 206 | 7B - CR 207

Time Table Officer

Chief Time Table Officer

HOD

Principal



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